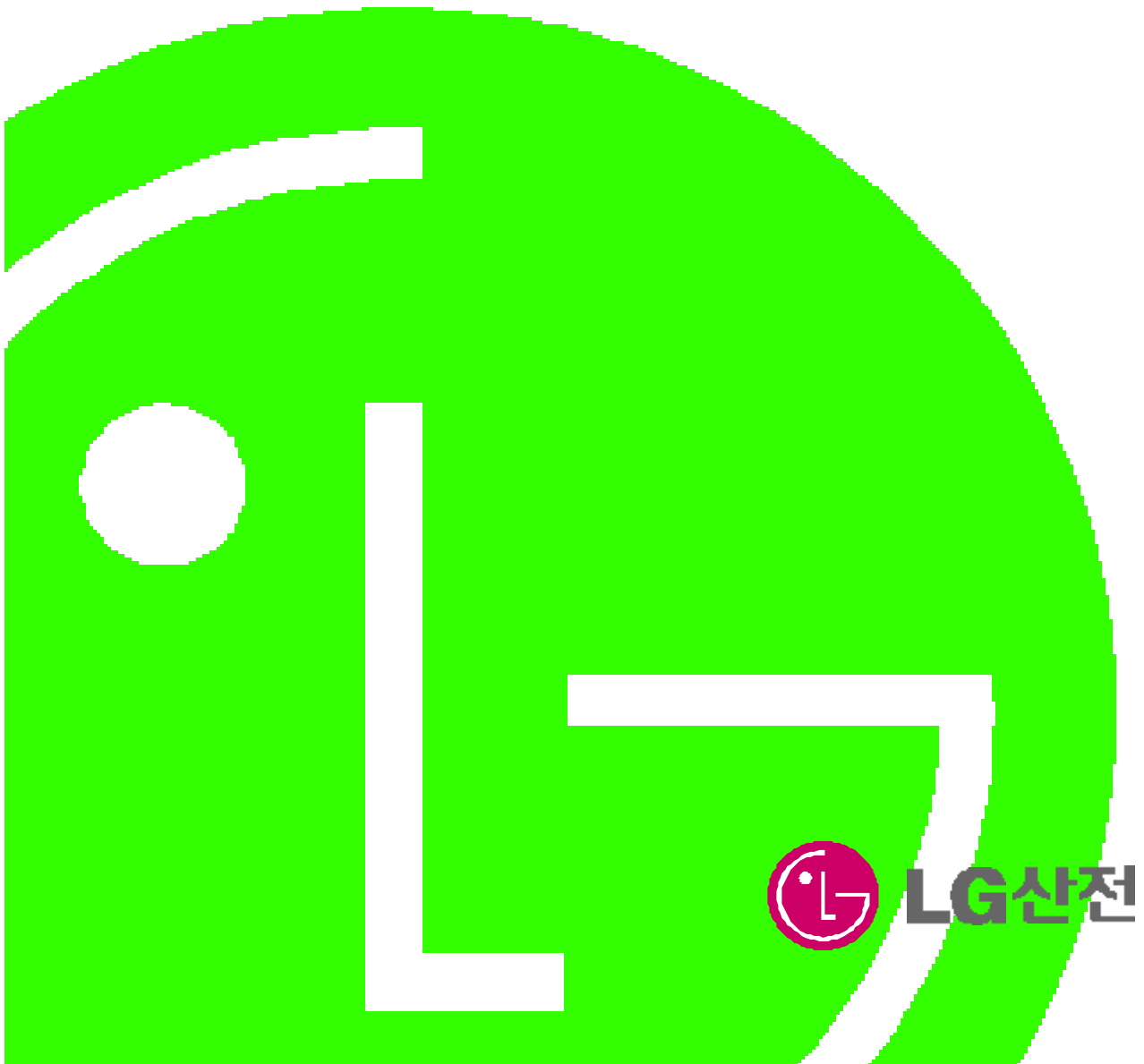


LG

GLOFA
MASTER-K

G3F-AD3A
G4F-AD3A
G6F-AD2A





1	1-1 ~ 12
----------------	-----------------

1.1		1 - 1
1.2		1 - 1
1.2.1.	– A	1 - 1
1.2.2.	- D	1 - 1
1.2.3.		1 - 2

2	2-1 ~ 2-13
----------------	-------------------

2.1		2 - 1
2.2		2 - 2
2.3		2 - 4
2.4		2 - 7
2.4.1		2 - 8
2.4.2		2 - 10
2.4.3	,	2 - 11
2.4.4		2 - 12
2.5		2 - 13

3	3-1 ~ 33
----------------	-----------------

3.1		3 - 1
3.1.1		3 - 1
3.1.2		3 - 1
3.2		3 - 2
3.2.1		3 - 2
3.2.2		3 - 2

4	4-1 ~ 49
----------------	-----------------

4.1	GMWIN	A/D		4 - 1
------------	--------------	------------	-------	-------

4.2.		4 - 3
4.2.1	(AD3INI)	4 - 3
4.2.2	_Array (AD3ARD)	4 - 4
4.2.3	_ (AD3RD)	4 - 4
4.2.4	(AD2INI)	4 - 5
4.2.5	_Array (AD2ARD)	4 - 6
4.2.6	_ (AD2RD)	4 - 6
4.3		4 - 7
4.3.1	(ADR3INI)	4 - 7
4.3.2	(ADR3RD)	4 - 8
4.4		4 - 9

5	GM	5-1 ~ 5-8
----------	-----------------	------------------

5.1	A/D	5 - 1
5.2	A/D BCD	5 - 5
5.3	I/O A/D	5 - 8

6		6-1 ~ 6-5
----------	--------	------------------

6.1		6 - 1
6.1.1	G3F-AD3A / G4F-AD3A	6 - 1
6.1.2	G6F-AD2A	6 - 2
6.2		6 - 3
6.2.1		6 - 3
6.2.2	(G3F-AD3A,G4F-AD3A)	6 - 3
6.2.3	(G6F-AD2A)	6 - 3
6.2.4	ﾌｼ/	6 - 4
6.2.5		6 - 4
6.2.6	SET	6 - 4
6.2.7		6 - 5
6.2.8		6 - 5

7	(/)	7-1 ~ 7-4
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7.1		7 - 1
7.1.1	(GET, GETP)	7 - 1
7.1.2	(PUT, PUTP)	7 - 2
7.2		7 - 3
7.2.1	(RGET)	7 - 3
7.2.2	(RPUT)	7 - 4

8 MK		8-1 ~ 8-7
-------------	-------	------------------

8.1		8 - 1
8.1.1	G3F-AD3A,G4F-AD3A	8 - 1
8.1.2	G6F-AD2A	8 - 2
8.2		8 - 3
8.2.1		8 - 3
8.2.2	A/D BCD	8 - 5
8.3.2	I/O A/D	8 - 7

9		9-1 ~ 93
----------	-------	-----------------

9.1	G3F-AD3A	9 - 1
9.2	G4F-AD3A	9 - 2
9.3	G6F-AD2A	9 - 3



G3F-AD3A/G4F-AD3A/G6F-AD2A

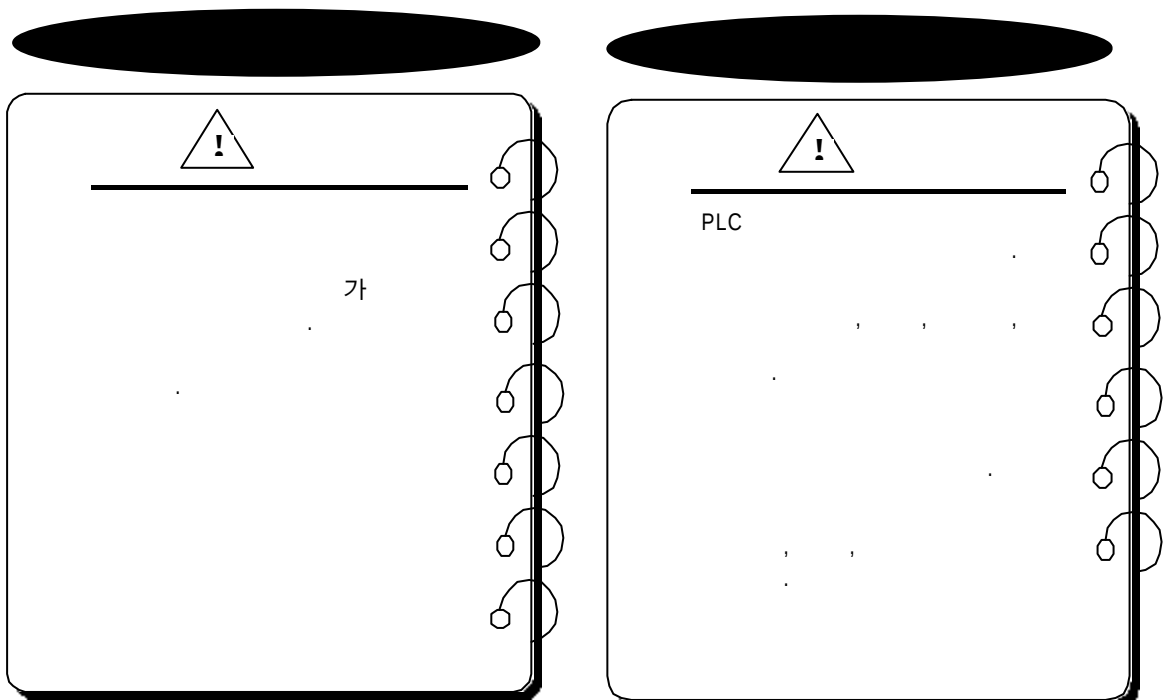
PLC
200S/300S/1000S CPU

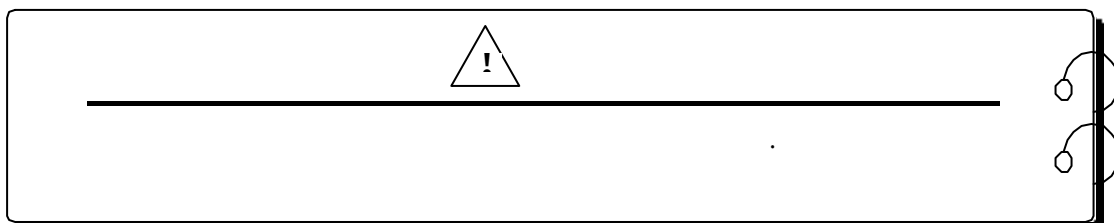
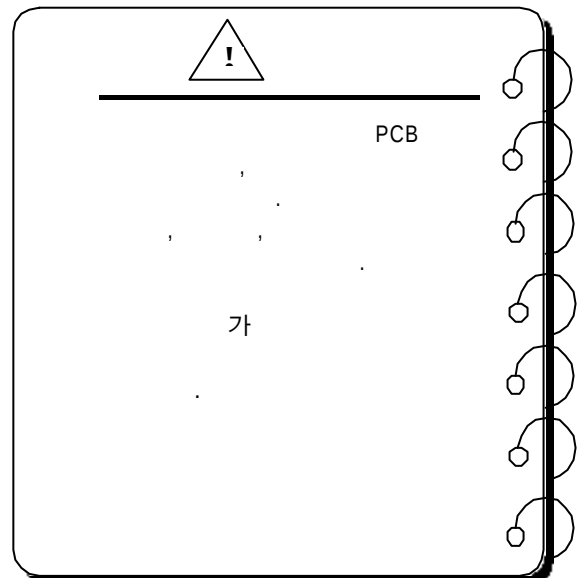
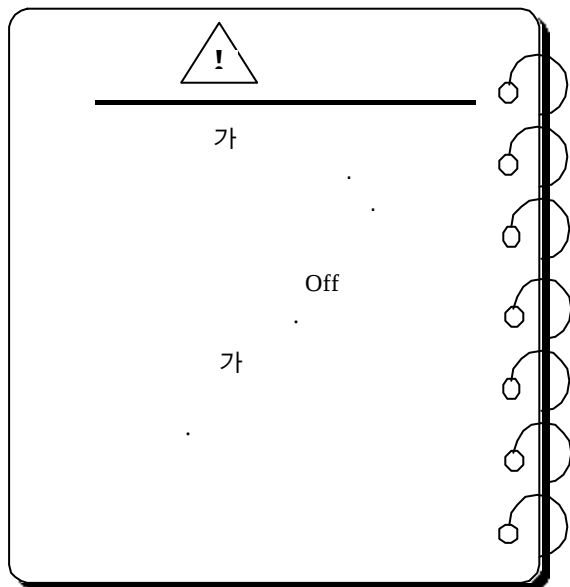
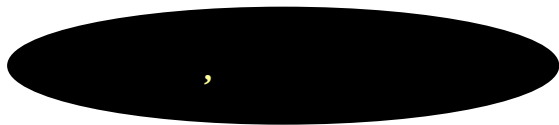
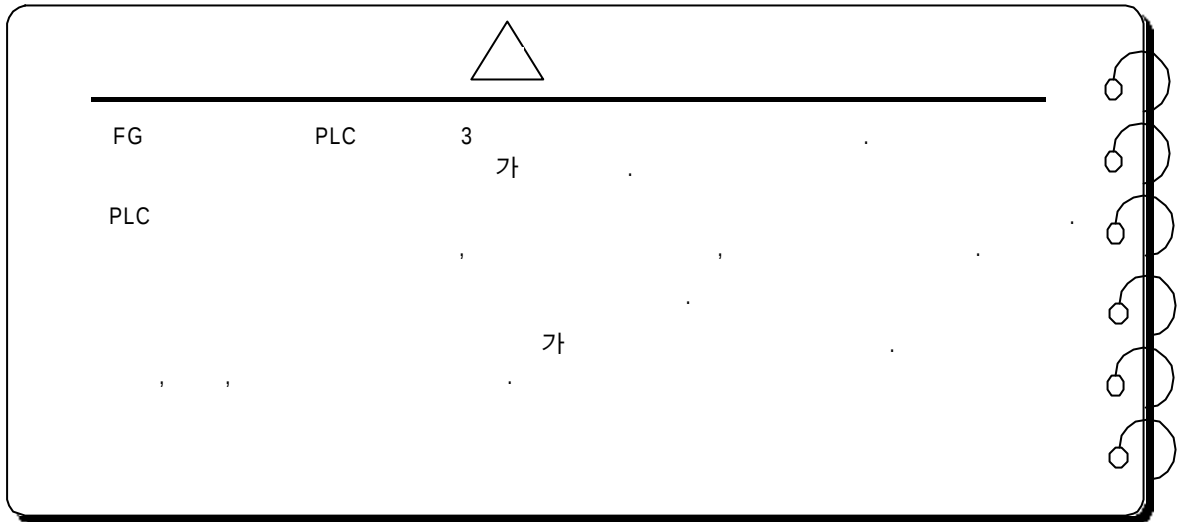
GLOFA GM3/4/6

MASTER -K



2 가





1

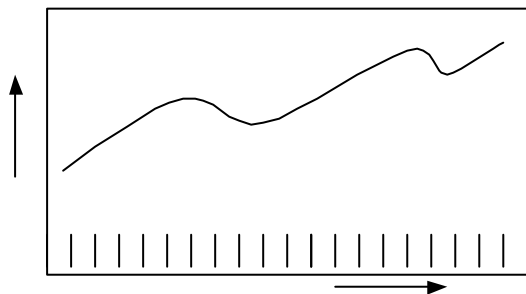
GLOFA PLC GM1/2/3/4/6 MASTER-K200S/300S/1000S CPU /
 GM1/2/3 K1000S G3F-AD3A, GM4 K300S G4F-AD3A,
 GM6 K200S G6F-AD2A(A/D)
 A/D PLC (가 12

1.1

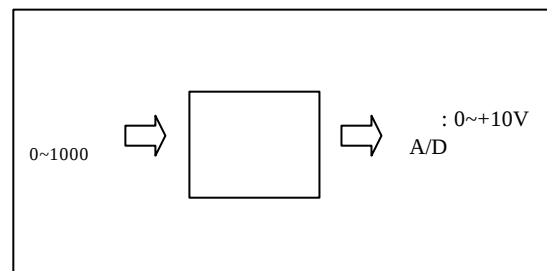
1) 1 4 /8 A/D 가
 G6F-AD2A 1 4 ,G3F-AD3A G4F-AD3A 1 8 A/D

2)
 (, G6F-AD2A G6F-DA2V G6F-DA2I GM6-PAFB
 .)

1.2.



[1.1]



[1.2]

1.2.1 - A

, , , , ,
 1.1

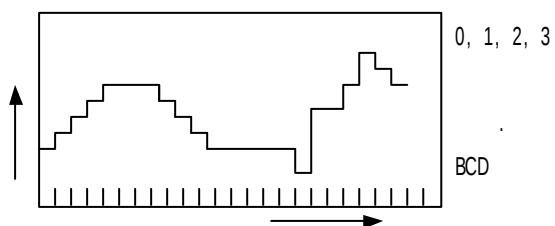
PLC

0 ~+10V

4 ~20mA

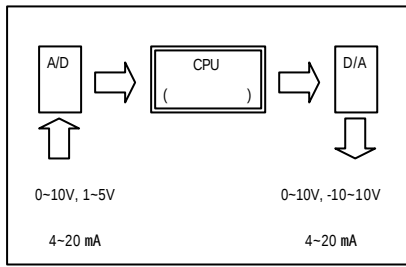
PLC

1.2.2 - D



[1.3]

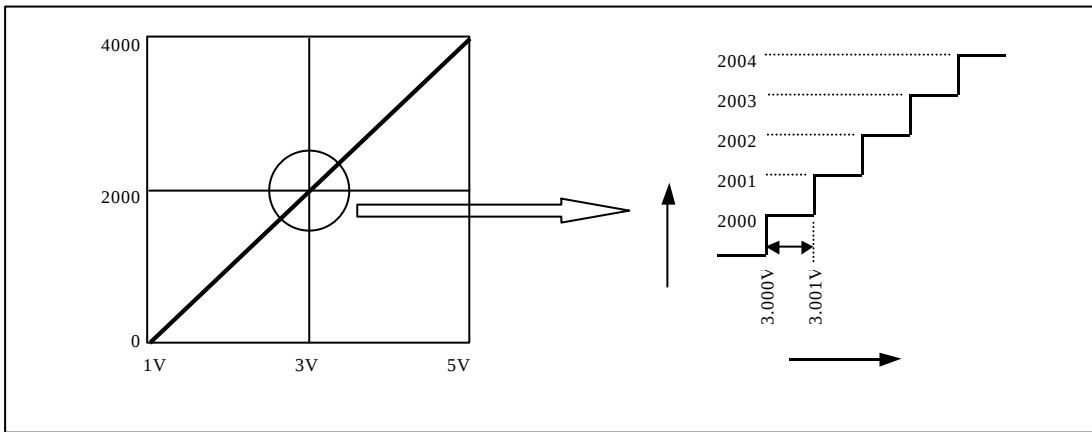
. On, Off 0 1



[1.4] PLC

1.2.3.

1)



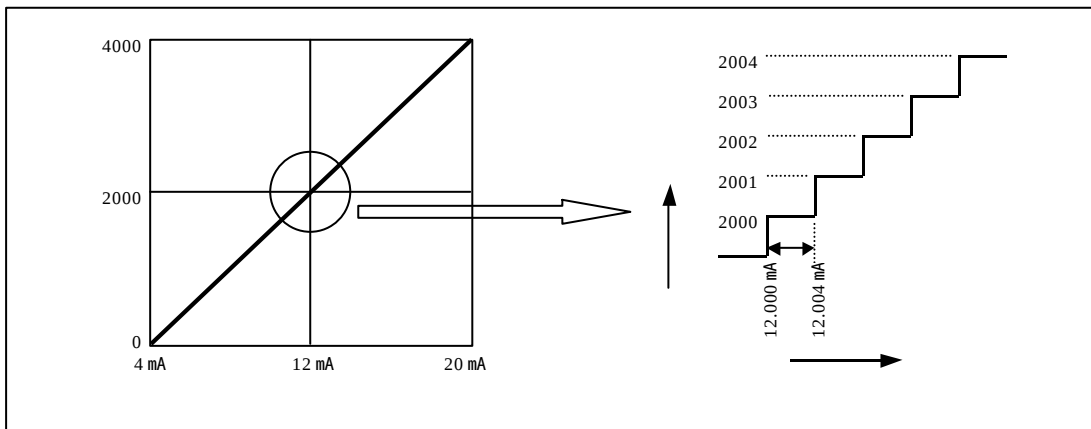
[1.5] A/D ()

가

1V 0 5V 4000

1mV가 1 1mV

2)



[1.6] A/D ()

4mA 0, 12mA 2000, 20mA 4000

4μA가 1 4μA

2

2.1

GLOFA GM

MASTER-K

2.1

.

No .							
1		0 ~ 55℃					
2		-25 ~+70 ℃					
3		5 ~ 95%RH,					
4		5 ~ 95%RH,					
5					-	IEC61131-2	
			가		X, Y, Z 10		
		10 ≤ f < 57Hz	-	0.075mm			
		57 ≤ f ≤ 150Hz	9.8m/s ² {1G}	-			
			가				
		10 ≤ f < 57Hz	-	0.035mm			
		57 ≤ f ≤ 150Hz	4.9m/s ² {0.5G}	-			
6		- 가 : 147 m/s²{15G} - 가 : 11ms : (X, Y, Z 3 3)				IEC61131-2	
7			± 1,500 V			LG	
			: 4kV ()			IEC61131-2 IEC1000-4-2	
			27 ~ 500 MHz, 10 V/m			IEC1131-2, IEC1000-4-3	
		/			(24V)	(24V)	IEC1131-2 IEC1000-4-4
				2kV	1kV	0.25kV	
8		가 , 가					
9		2,000m					
10		2					
11							

1) IEC(International Electrotechnical Commission :)

: 가

2)

: 2 ,

2.2.

A/D

2.2, 2.3

1) G3F-AD3A, G4F-AD3A

		G3F-AD3A	G4F-AD3A
		16	
		DC1 5V (600k Ω)	
		DC0 10V (600k Ω)	
		DC4 20mA (250 Ω)	
	/	(On: , Off:)	
		12	(0 4000)
	DC1 5V	1mV (1/4000)	
	DC0 10V	2.5mV (1/4000)	
	DC4 20mA	4 μ A (1/4000)	
		$\pm 0.5\%$ [(Full Scale)]	
		5.0ms/	
		: ± 15 V, : 25mA	
		8 /	
		PLC	()
		20	
		500 mA	
		310g	280g

[2.2]

A/D

2) G6F-AD2A

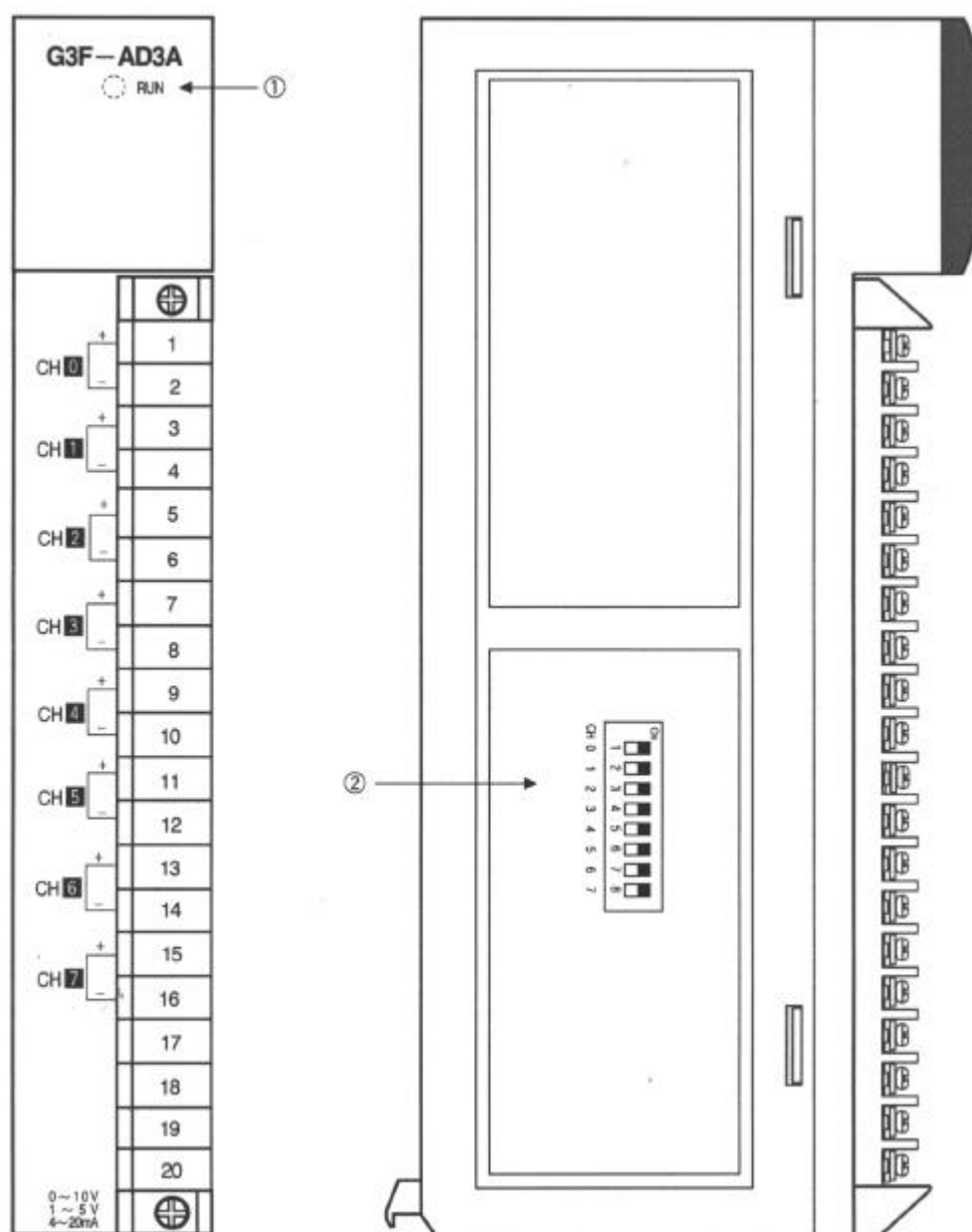
		16
		DC 1 5V (1M) DC 0 ~ 10V (1M) DC -10 10V (1M)
		DC 4 20mA (250Ω)
	/	. (V I .) .
		·12 (-48~4047, -2048 ~ 2047) .
	DC 1 5V	1mV (1/4000)
	DC 0~10V	2.5mV(1/4000)
	DC -10~10V	5mV (1/4000)
	DC 4 20mA	4μA (1/4000)
		± 0.5% [(Full Scale)]
		5.0ms/
		:15V, : 25mA
		4 /
		PLC ()
		18
	DC+5V	40mA
	DC+15V	50mA
	DC-15V	20mA
		200g

[2.3]

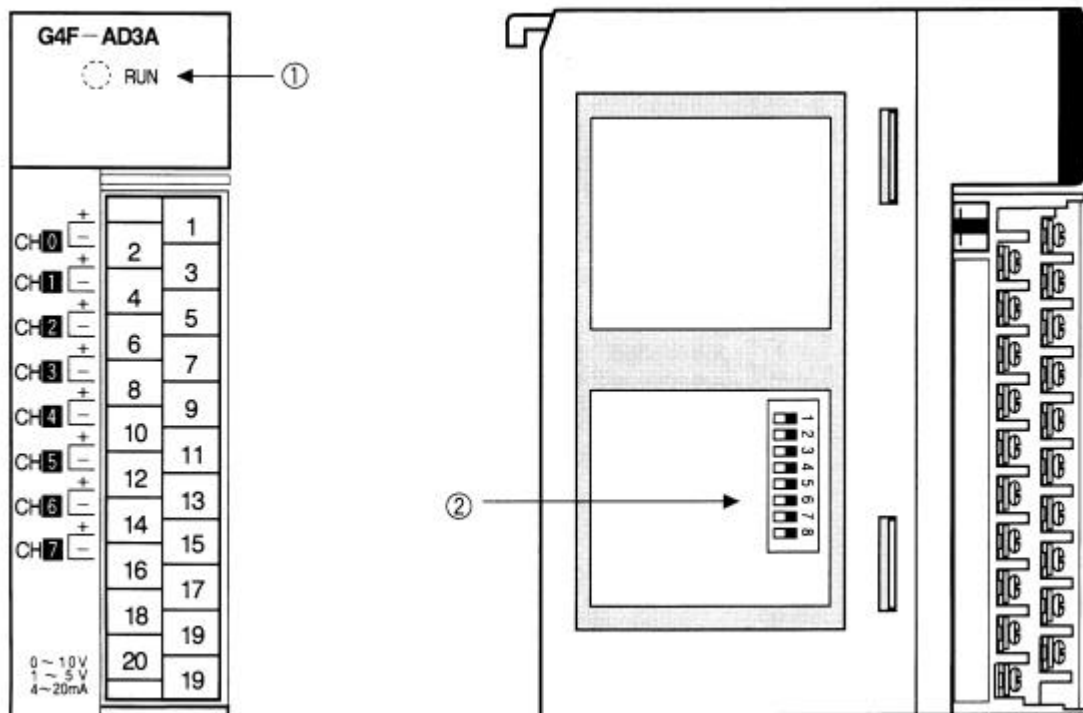
A/D		
G6F-AD2A	GM6-PAFB	

2.3

1) G3F-AD3A

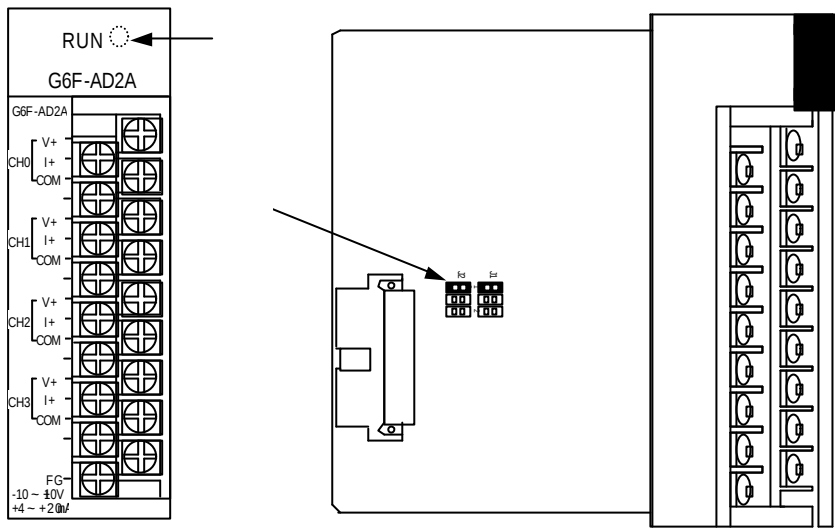


2) G4F-AD3A



No	
①	RUN LED G3F-AD3A G4F-AD3A
②	/ 1)  Off 2)  On

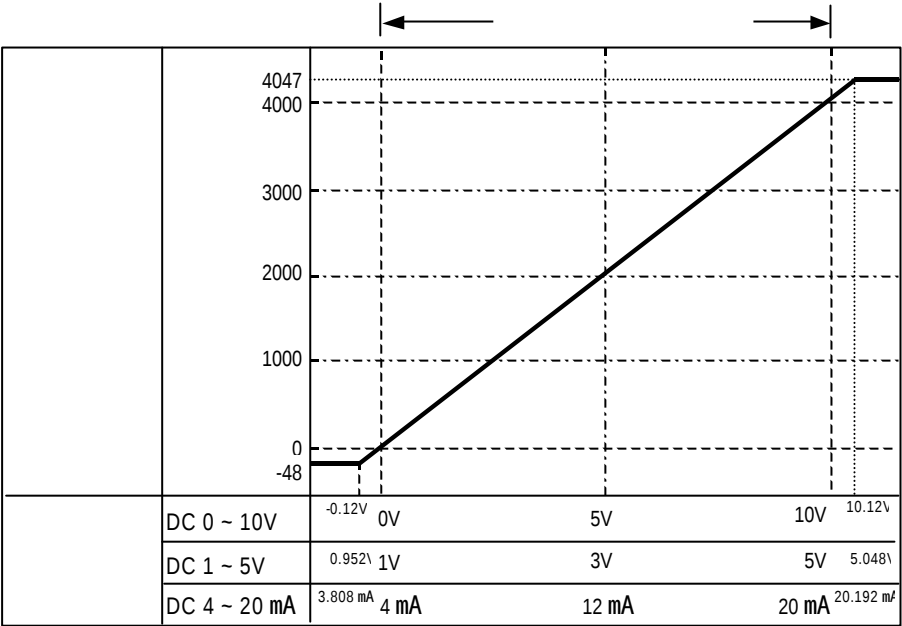
3)G6F-AD2A



No			
①	RUN LED		
	G6F-AD2A		
②			
		DC 1~5V	J1 
			J2 
		DC 0~10V	J1 
			J2 
		DC 10~10V	J1 
			J2 
		DC 4~20mA	J1 
			J2 

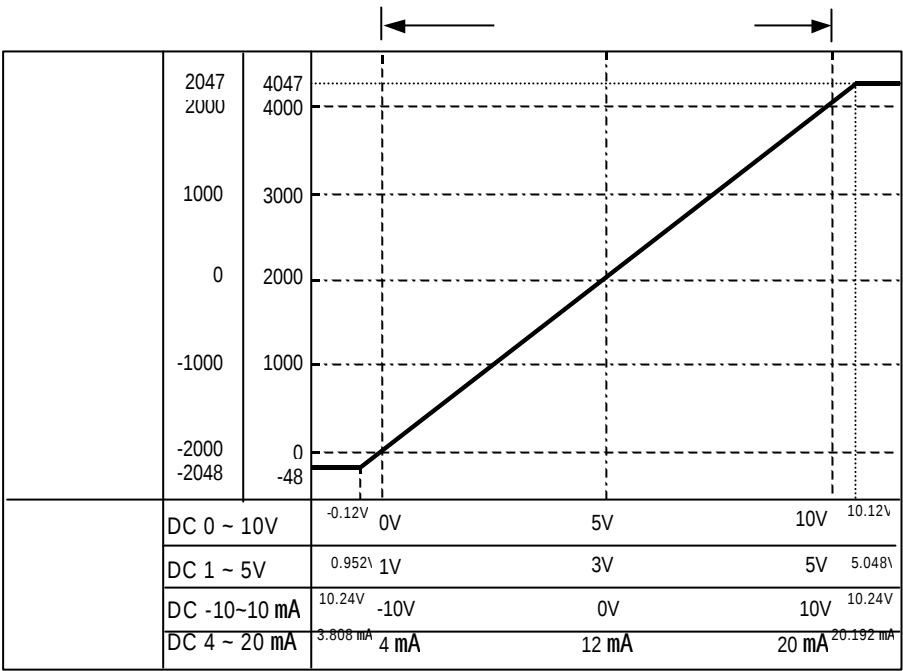
2.4

- PLC ()
G3F-AD3A, G4F-AD3A 2.1 , G6F-AD2A
2.2 .
-A/D / 가 /



[2.1] (G3F-AD3A, G4F-AD3A)

1.	4047	-48
2.	+15V,	+25mA



[2.2] (G6F-AD2A)

1.	4047	-48 (2047	-2048)
	4047	-48 (2047	-2048)
2.	+15V,	+25mA	

2.4.1

1) G3F-AD3A, G4F-AD3A

Off

/

- Off :

On

1

2

3

4

5

6

7

8

CH0 1 2 3 4 5 6 7 ←

()

/

Off

(1) DC 0 ~10V

	(V)						
	-0.12	0	2.5	5	7.5	10	10.12
	-48	0	1000	2000	3000	4000	4047

(2) DC 1 ~5V

	(V)						
	0.952	1	2	3	4	5	5.048
	-48	0	1000	2000	3000	4000	4047

2) G6F-AD2A

-
-

(1) DC 1 ~ 5V

	(V)							
	0.952	1	2	3	4	5	5.048	
	-48	0	1000	2000	3000	4000	4047	
	-2048	-2000	-1000	0	1000	2000	2047	

(2) DC 0 ~10V

	(V)							
	-0.12	0	2.5	5	7.5	10	10.12	
	-48	0	1000	2000	3000	4000	4047	
	-2048	-2000	-1000	0	1000	2000	2047	

(3) DC -10 ~ 10V

	(V)							
	-10.24	-10	-5	0	5	10	10.24	
	-48	0	1000	2000	3000	4000	4047	
	-2048	-2000	-1000	0	1000	2000	2047	

2.4.2

1) G3F-AD3A, G4F-AD3A

-

/

On

.

/

- On :

On

12345678

()

CH01234567 ←

	(mA)						
	3.808	4	8	12	16	20	20.192
	-48	0	1000	2000	3000	4000	4047

2) G6F-AD2A

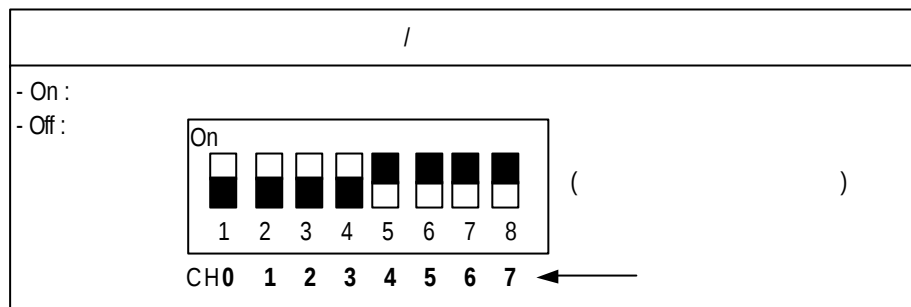
	(V)							
	3.808	4	8	12	16	20	20.192	J1 123 J2
	-48	0	1000	2000	3000	4000	4047	
	-2048	-2000	-1000	0	1000	2000	2047	

V I

2.4.3

1) G3F-AD3A, G4F-AD3A

- , / Off,
On
) : 0 ~ 3 : 4 ~ 7



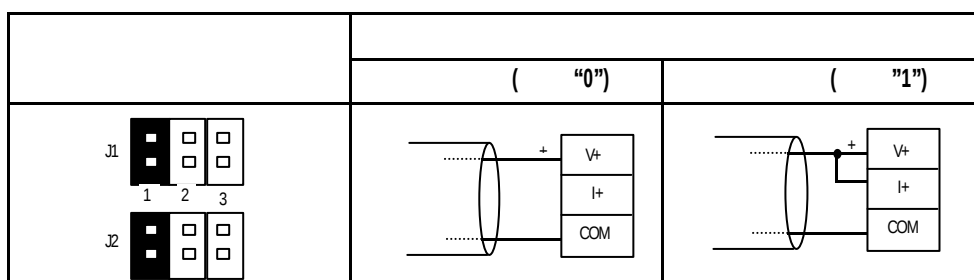
(V)	0~10V	-0.12	0	2.5	5	7.5	10	10.12
	1~5V	0.952	1	2	3	4	5	5.048
(mA)	(mA)	3.808	4	8	12	16	20	20.192
		-48	0	1000	2000	3000	4000	4047

2) G6F-AD2A

- , DC 1-5V
- /

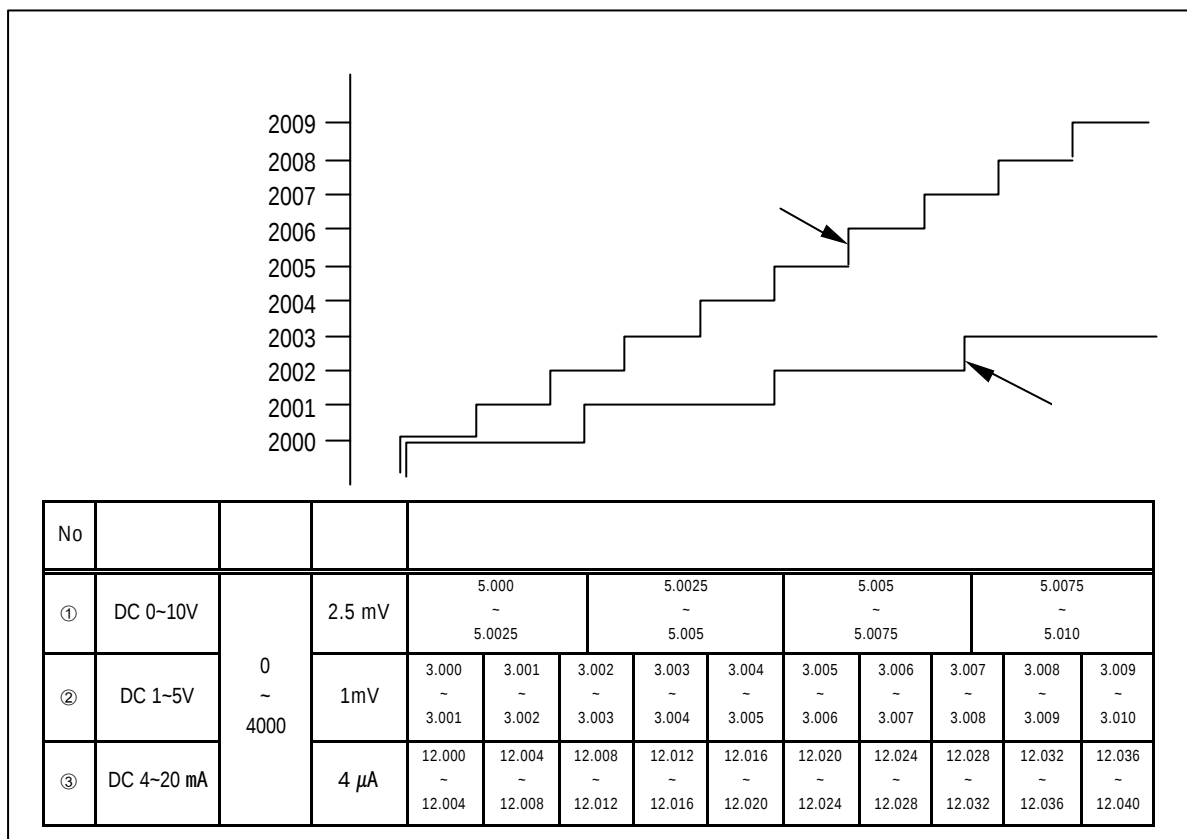
1~5V	0.952	1	2	3	4	5	5.048
(4~20mA)	3.808	4	8	12	16	20	20.192
	-48	0	1000	2000	3000	4000	4047
	-2048	-2000	-1000	0	1000	2000	2047

) :0, :1

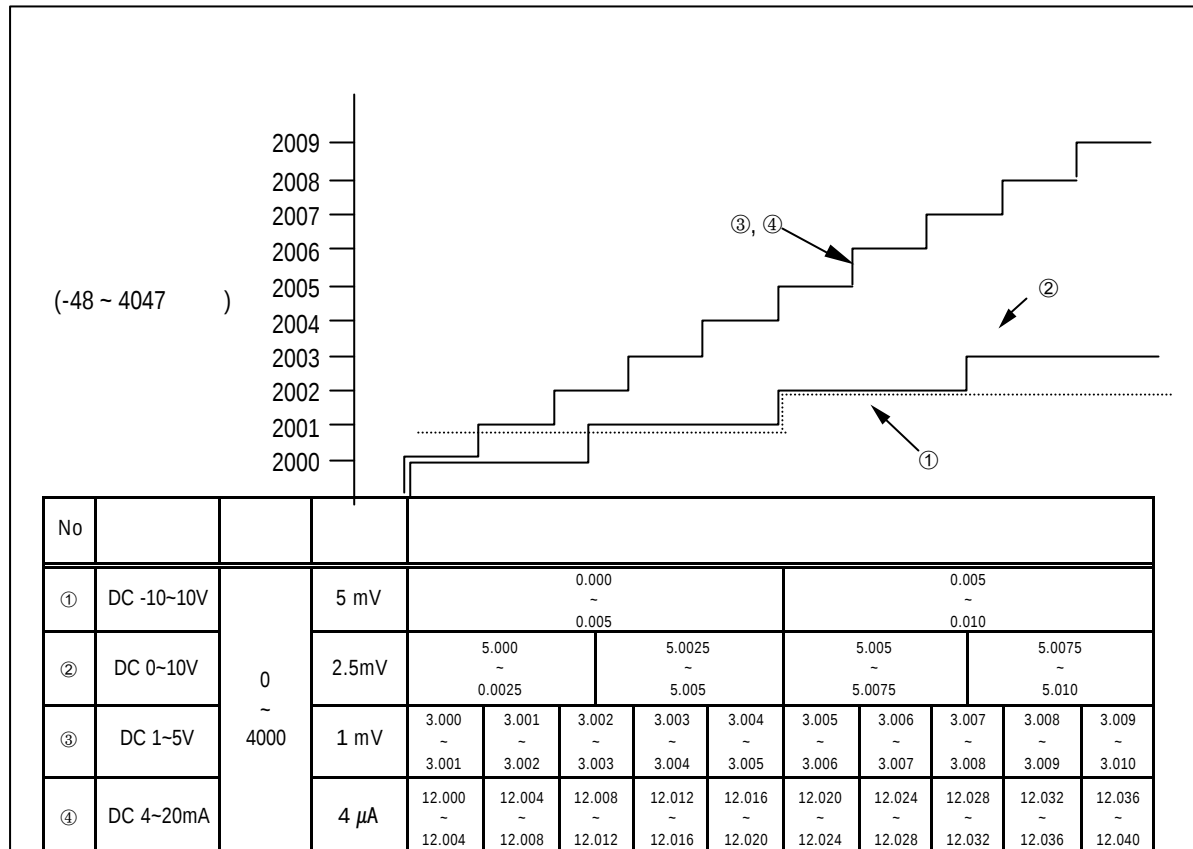


2.4.4

1) G3FAD3A, G4FAD3A



2) G6F-AD2A



2.5

- G3F-AD3A, G4F-AD3A, G6F-AD2A

1) : 2 ~ 255

2)

= X X

) : 4, 가 50

$$50 \times 4 \times 5 = 1000\text{ms}$$

3

3.1

3.1.1

가

1)

- 가
- 가
-
-
- 가 0-55

2)

- 가 PLC 가 가
-
- (panel)
- 50mm
-

3.1.2

A/D

1)

2)

PCB

3)

가

4)

3.2

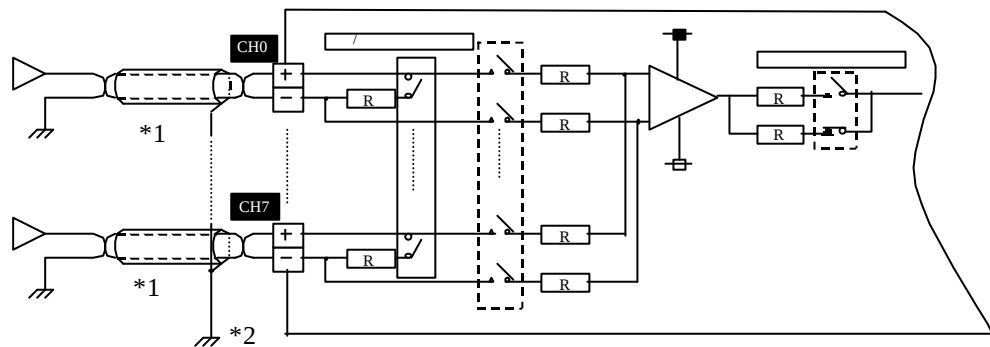
3.2.1

- 1) A/D
- 2) , AWG22(0.3 mm²)
- 3) 가 ,
- 4) 가
- 5)

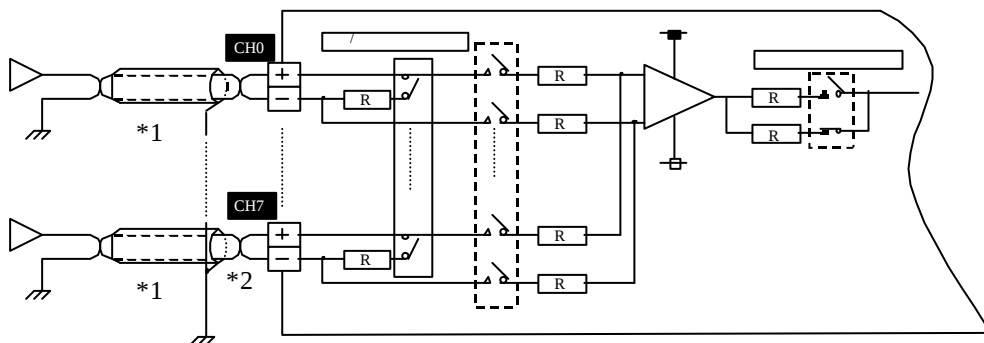
3.2.2

1) G3F-AD3A, G4F-AD3A

(1)



(2)

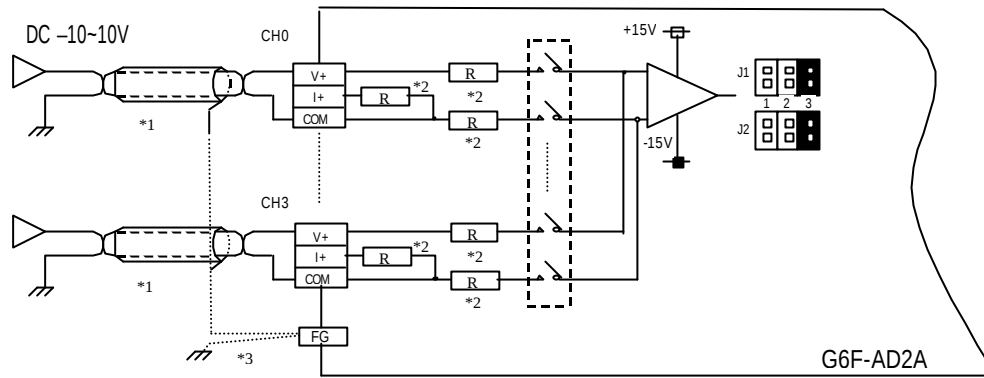


*1 : 2

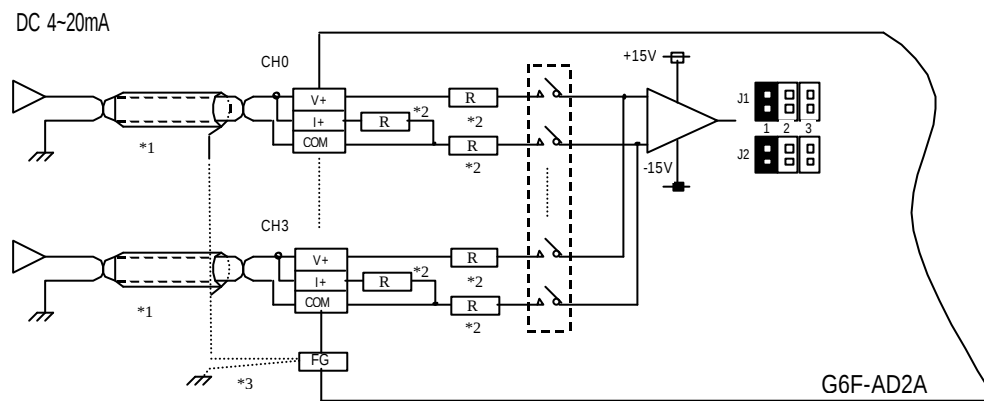
*2 : 가

2) G6F-AD2A

(1)



(2)



*1 : 2

*2 :

*3 : 가

가

FG

GMWIN

A/D

No	G3F-AD3A		G4F-AD3A		G6F-AD2A		
1	AD3INI	ADR3INI	AD3INI	ADR3INI	AD2INI	ADR62INI	
2	AD3ARD	ADR3RD	AD3ARD	ADR3RD	AD2ARD	ADR62RD	A/D ()
3	AD3RD	-	AD3RD	-	AD2RD	-	A/D ()

G3F-AD3A G4F-AD3A

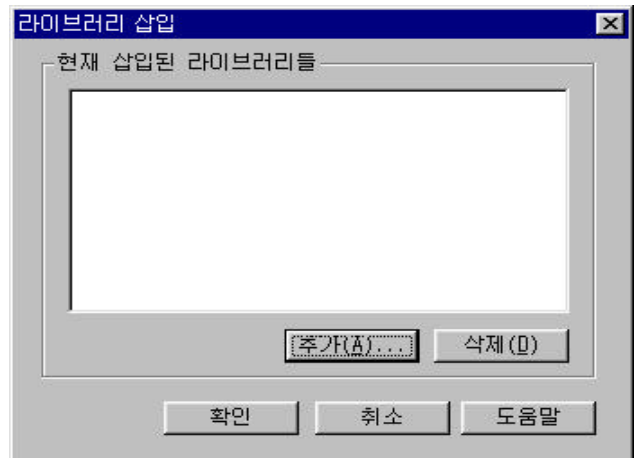
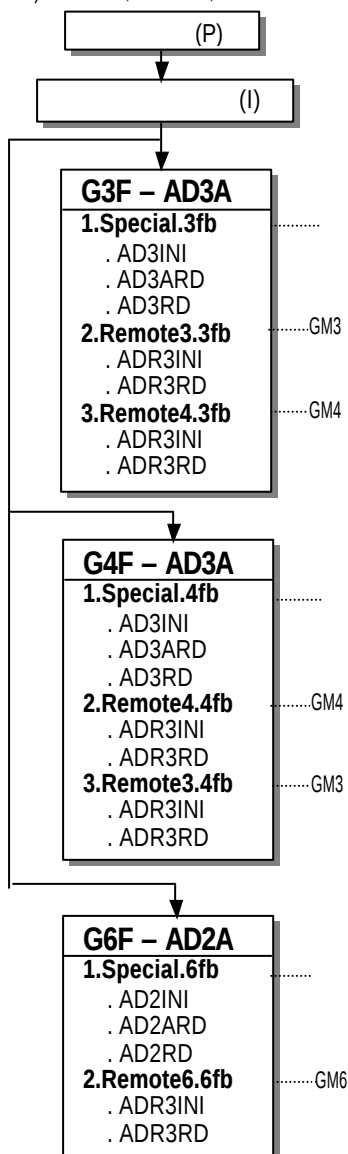
4.1 GMWIN A/D

GMWIN

가

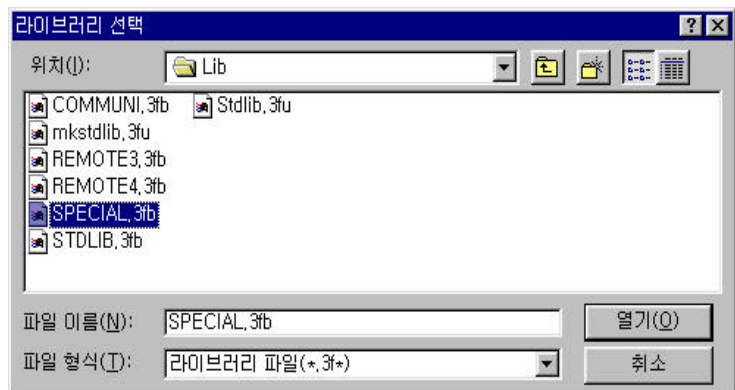
가

1) G3F-AD3A, G4F-AD3A, G6F-AD2A



■ 가(A)

■ GMWIN 3.1 (G3F-AD3A)



4.2

4.2.1

(AD3INI) ----- G3F-AD3A, G4F-AD3A

A/D

			Datatype	
AD3INI REQ DONE BASE STAT SLOT ACT CH TYPE AVG_ EN AVG_ NUM		REQ	BOOL	"0→1"
		BASE	USINT	A/D : 0 3
		SLOT	USINT	A/D : 0 7
		CH	BOOL[8]	"0" "1"
		TYPE	BOOL[8]	"0" DC1 5, DC4 20mA "1" DC0 10 "1"
		AVG_	BOOL[8]	가 "0" "1"
		AVG_	USINT[8]	: 2 255
		DONE	BOOL	"1" , 가 "0" , 가
		STAT	USINT	가
		ACT	BOOL[8]	"1" , "0"

Datatype BOOL[8] USINT[8] 가 8

4.2.2 _Array (AD3ARD) ----- G3F-AD3A, G4F-AD3A

A/D
DATA

A/D

			Datatype	
AD3ARD REQ DONE BASE STAT SLOT ACT CH DATA		REQ	BOOL	"0→1"
		BASE	USINT	A/D : 0 3
		SLOT	USINT	A/D : 0 7
		CH	BOOL[8]	"0" "1"
		DONE	BOOL	"1" , 가 "0" "1" , 가
		STAT	USINT	가
		ACT	BOOL[8]	"1" , "0" "0"
		DATA	INT[8]	A/D : -48 4047
Datatype BOOL[8] INT[8] 가 8				

4.2.3 _ (AD3RD) ----- G3F-AD3A, G4F-AD3A

A/D

A/D

DATA

			Datatype	
AD3RD REQ DONE BASE STAT SLOT DATA CH		REQ	BOOL	"0→1"
		BASE	USINT	A/D : 0 3
		SLOT	USINT	A/D : 0 7
		CH	USINT	: 0 7
		DONE	BOOL	"1" , 가 "0" "1" , 가
		STAT	USINT	가
		DATA	INT	A/D : -48 4047

4.2.4

(AD2INI) ----- G6F-AD2A

A/D

			Datatype	
AD2INI REQ DONE BASE STAT SLOT ACT CH DATA TYPE AVG_ EN AVG_ NUM		REQ	BOOL	"0→1"
		BASE	USINT	A/D : 0
		SLOT	USINT	A/D : 0 7
		CH	BOOL[4]	"0" "1"
		DATA TYPE	BOOL[4]	"0" : -48 ~ 4047 "1" : -2048 ~ 2047
		AVG_ EN	BOOL[4]	가 "0" "1"
		AVG_ NUM	USINT[4]	: 2 255
		DONE	BOOL	"1" , 가 "0" "1" , 가
		STAT	USINT	가
		ACT	BOOL[4]	"1" , "0"

Datatype BOOL[4] USINT[4] 가 4

4.2.5 Array (AD2ARD) ----- G6F-AD2A

A/D

A/D

DATA

			Datatype	
AD2ARD REQ DONE BASE STAT SLOT ACT CH DATA		REQ	BOOL	"0→1"
		BASE	USINT	A/D : 0
		SLOT	USINT	A/D : 0 7
		CH	BOOL[4]	"0" "1"
		DONE	BOOL	"1" 가 "0" 가
		STAT	USINT	가
		ACT	BOOL[4]	"1" "0" "0"
		DATA	INT[4]	A/D : -48 4047 -2048 ~ 2047

Datatype	BOOL[4]	INT[4]	가 4
----------	---------	--------	-----

4.2.6 (AD2RD) ----- G6F-AD2A

A/D

A/D

DATA

			Datatype	
AD2RD REQ DONE BASE STAT SLOT DATA CH		REQ	BOOL	"0→1"
		BASE	USINT	A/D : 0
		SLOT	USINT	A/D : 0 7
		CH	USINT	: 0 3
		DONE	BOOL	"1" 가 "0" 가
		STAT	USINT	가
		DATA	INT	A/D : -48 4047 -2048 ~ 2047

4.3

4.3.1.

(ADR3INI)

(G3F-AD3A/G4F-AD3A : ADR3INI, G6F-AD2A : ADR62INI)

A/D

I/O

			Datatype	
G3F-AD3A/ G4F-AD3A (G6F-AD2A) ADR3INI (ADR62INI) REQ NDR NET_ ERR NO ST_N STAT O BASE ACT SLOT CH TYPE AVG_ EN AVG_ EN AVG_ NUM		REQ	BOOL	Edge "0→1" ( Edge)
		NET_NO	USINT	: 0 7
		ST_NO	USINT	I/O : 0 63
		BASE	USINT	A/D : 0 3 (, G6F-AD2A : 0)
		SLOT	USINT	A/D : 0 7
		CH	BOOL[8]	"0" "1"
		TYPE	BOOL[8]	"0" DC1 5, DC4 20mA "1" DC0 10 "1"
		AVG_EN	BOOL[8]	가/ "0" "1"
		AVG_NUM	USINT[8]	: 2 255
		NDR	BOOL	"0" "1" "1"
		ERR	BOOL	가 "1" 가 , "1" "0"
		STAT	USINT	가
		ACT	BOOL[8]	"0" "1" "0"

G3F-AD3A G4F-AD3A Datatype BOOL[8] USINT[8] 가 8
 G6F-AD2A Datatype BOOL[4] USINT[4] 가 4

4.3.2

(ADR3RD)

(G3F-AD3A/G4F-AD3A : ADR3RD, G6F-AD2A : ADR62RD)

A/D

A/D

DATA

			Datatype	
G3F-AD3A/ G4F-AD3A (G6F-AD2A) ADR3RD (ADR62RD) REQ NDR NET_ ERR NO ST_N STAT O BASE ACT SLOT DATA CH		REQ	BOOL	Edge "0→1" ( Edge)
		NET_	USINT	: 0 7
		ST_N	USINT	I/O : 0 63
		BASE	USINT	A/D : 0 3 (, G6F-AD2A : 0)
		SLOT	USINT	A/D : 0 7
		CH	BOOL[8]	"0" "1"
		NDR	BOOL	"0" "1" "1"
		ERR	BOOL	가 "1" 가 , "1" "0"
		STAT	USINT	가
		ACT	BOOL[8]	"0" "1" "0"
		DATA	INT[8]	A/D : -48 4047

G3F-AD3A G4F-AD3A Datatype BOOL[8] INT[8] 가 8
 G6F-AD2A Datatype BOOL[4] USINT[4] 가 4

4.4

STAT

STAT						
				Array		
0			○	○	○	-
1		가	○	○	○	(4.2)
2		H/W	○	○	○	A/S
3		가	○	○	○	A/D
4		A/D	○	○	○	A/D
5		A/D	○	○	○	A/D
6		가	-	-	○	
7		A/D H/W	○	○	○	A/S
8		A/D	○	○	○	A/S
9			-	○	○	
17			○	-	-	2 255
128		H/W	○	○	-	
129		가	○	○		(4.3)
131		가	○	○		A/D
133		A/D	○	○		A/D
135		A/D H/W	○	○		A/S
136		A/D	○	○		A/S
137			-	○		
145			○	-		2 255

5 GM

가 .

5.1 A/D

1)

GM3-PA1A	GM3-CPUA	G3F-AD3A	G3Q-RY4A
----------	----------	----------	----------

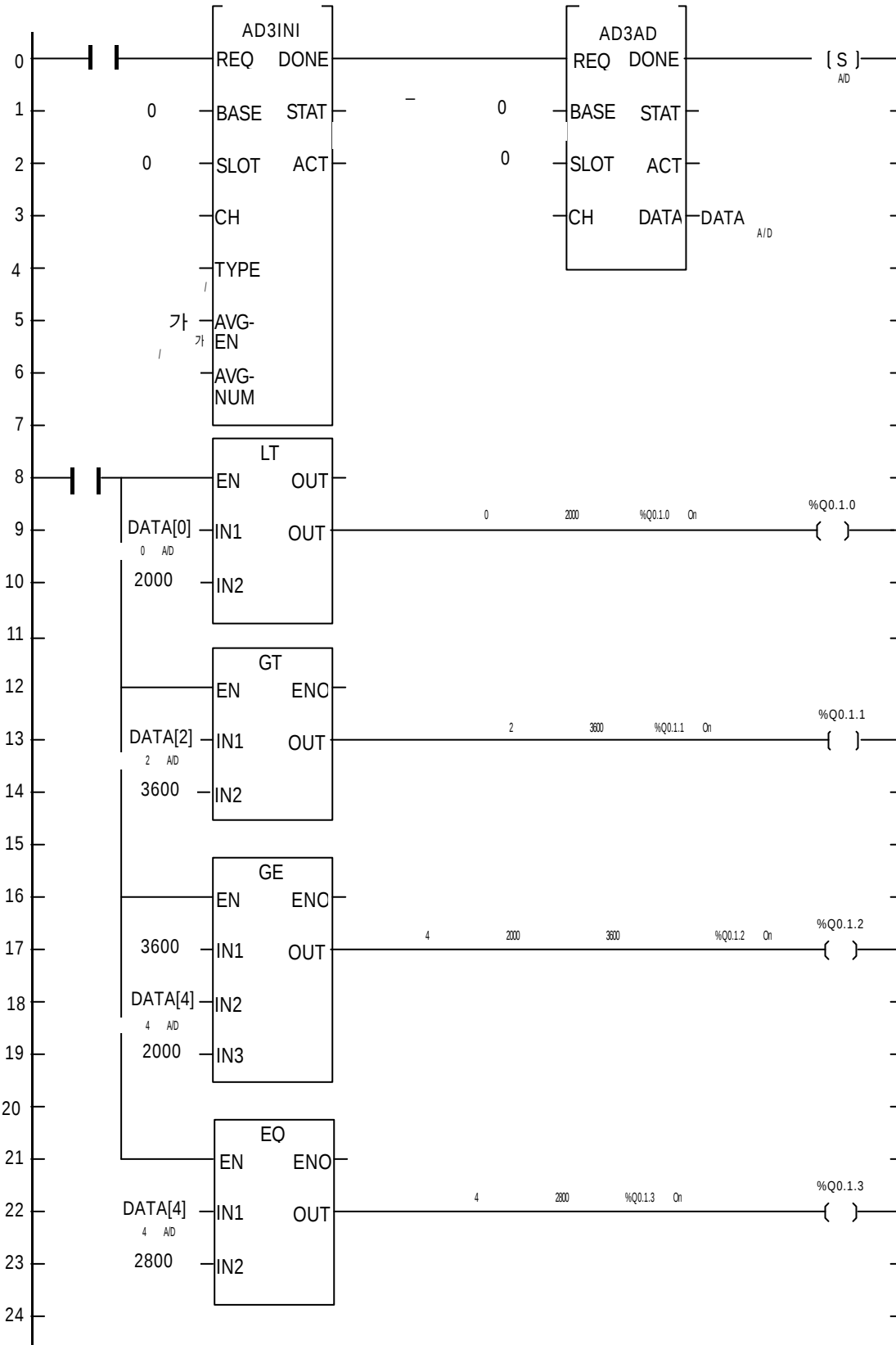
2)

- (1) : 0, 2, 4
(2) : DC 4~20mA
(3) : 2 (100), 4 (50)

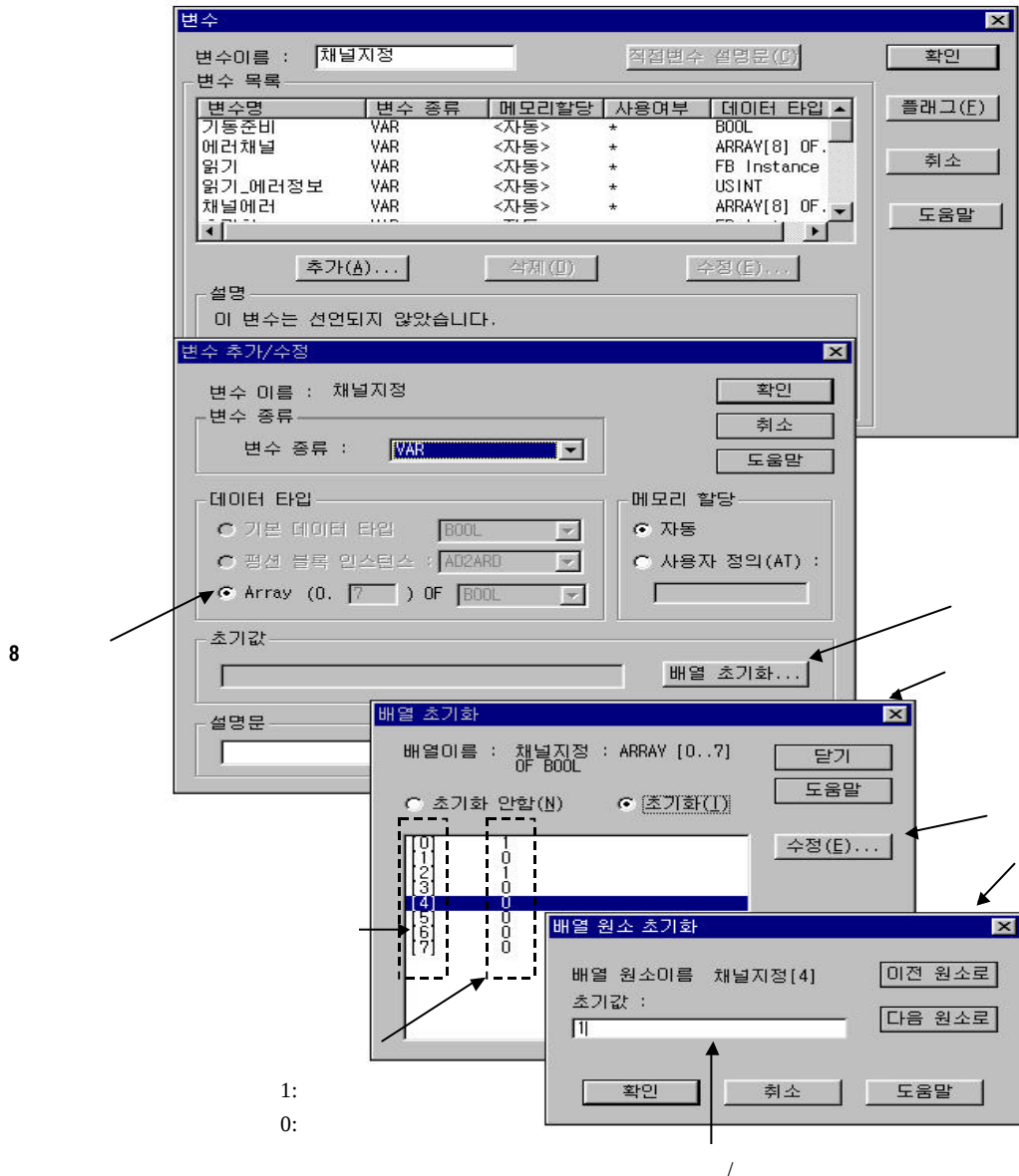
3)

- | | | | | | |
|-----|---|------|---------|---------|----|
| (1) | 0 | 2000 | %Q0.1.0 | On | |
| (2) | 2 | 3600 | %Q0.1.1 | On | |
| (3) | 4 | 2000 | 3,600 | %Q0.1.2 | On |
| (4) | 4 | 2800 | %Q0.1.3 | On | |

4)



5) ()

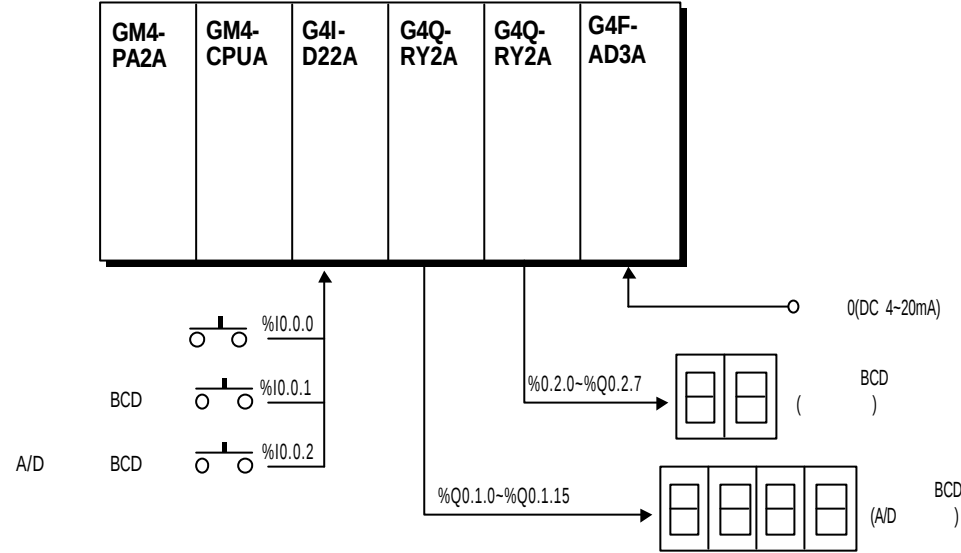


6)

Variable Name		Var-Kind	Data Type	(AT Address) (Initial Value)
	:	VAR	:	BOOL
	:	VAR	:	ARRAY [0..7] OF BOOL
	:	VAR	:	FB Instance
—	:	VAR	:	USINT
	:	VAR	:	ARRAY [0..7] OF BOOL
				:= { 0, 0, 0, 0, 0, 0, 0, 0 }
	:	VAR	:	ARRAY [0..7] OF BOOL
	:	VAR	:	ARRAY [0..7] OF BOOL
				:= { 1, 0, 1, 0, 1, 0, 0, 0 }
	:	VAR	:	FB Instance
—	:	VAR	:	USINT
	:	VAR	:	BOOL
가	:	VAR	:	ARRAY [0..7] OF BOOL
				:= { 0, 0, 1, 0, 1, 0, 0, 0 }
	:	VAR	:	ARRAY [0..7] OF USINT
				:= { 0, 0, 100, 0, 50, 0, 0, 0 }
DATA	:	VAR	:	ARRAY [0..7] OF INT

5.2 A/D BCD

1)



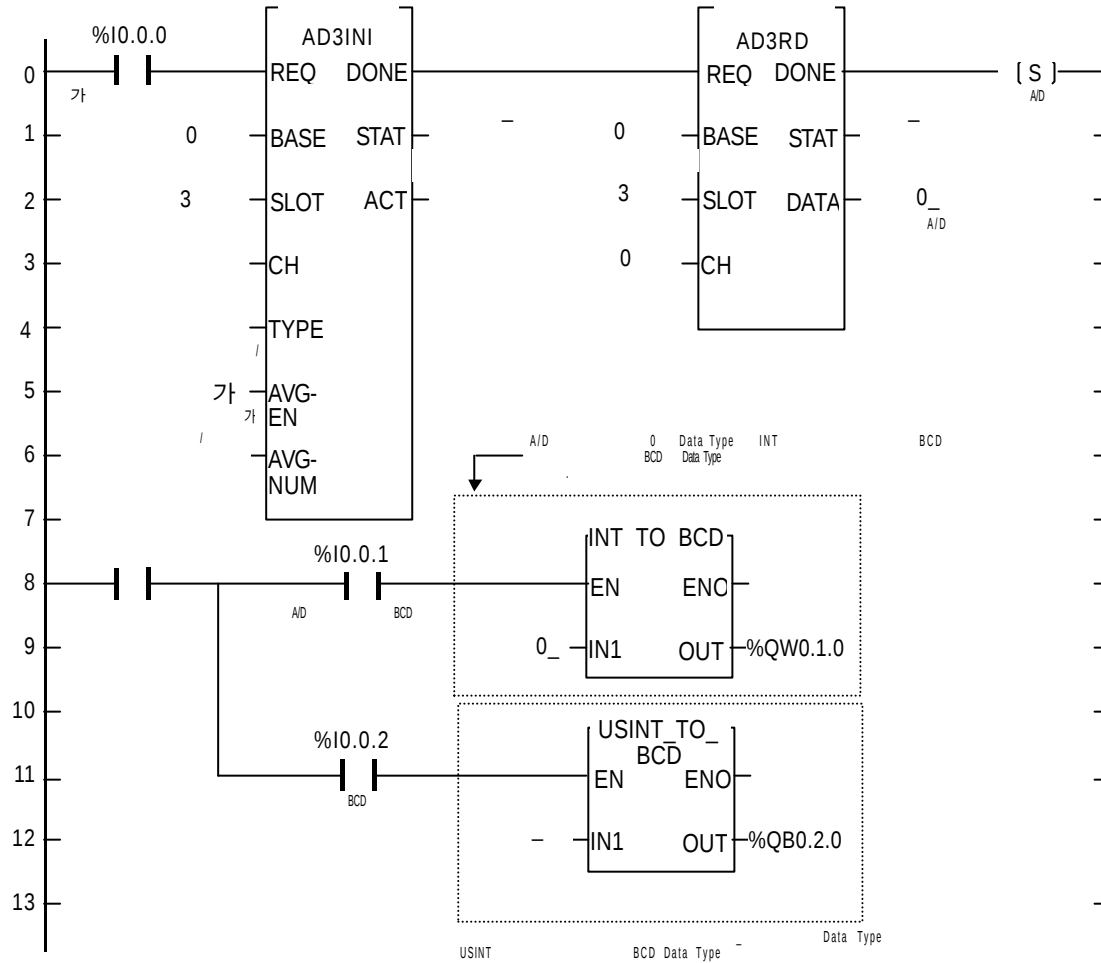
2)

- (1) : 0
- (2) : (DC 4~20mA)
- (3) : 10

3)

- (1) %I0.0 On AD
- (2) %I0.1 On AD BCD (%Q0.10~%Q0.115)
- (3) %I0.2 On BCD (%Q0.20~%Q0.27)

4)

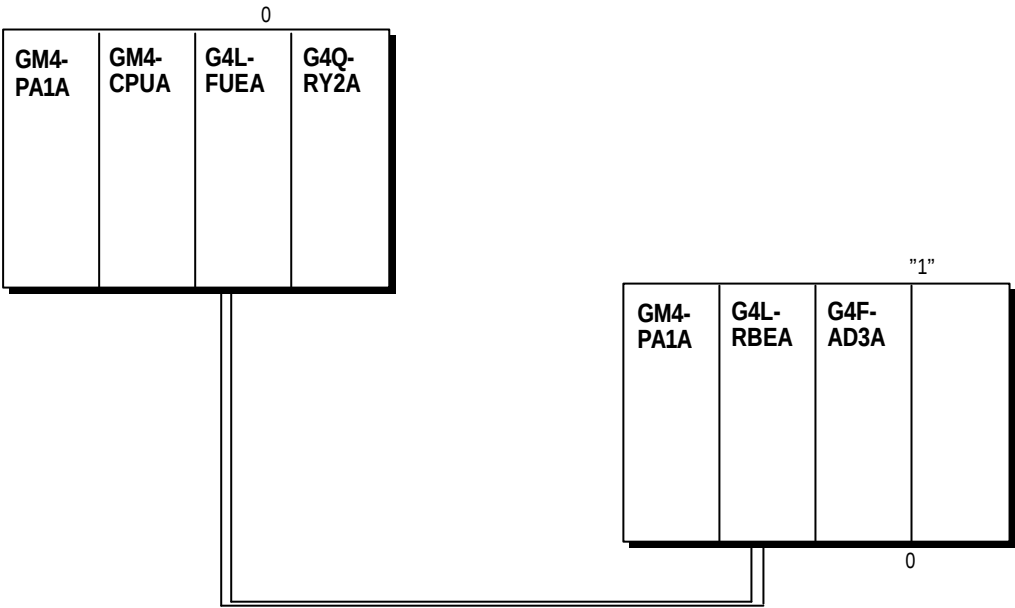


5)

Variable	Name	Var_Kind	Data Type	(AT Address) (Initial Value)
		: VAR	: ARRAY[0..7] OF BOOL	
		: VAR	: FB Instance	
		: VAR	: USINT	
		: VAR	: ARRAY[0..7] OF BOOL	
				:= { 0, 0, 0, 0, 0, 0, 0, 0, }
0_		: VAR	: INT	
		: VAR	: ARRAY[0..7] OF BOOL	
				:= { 1, 0, 0, 0, 0, 0, 0, 0, }
		: VAR	: FB Instance	
		: VAR	: USINT	
		: VAR	: BOOL	
가		: VAR	: ARRAY[0..7] OF BOOL	
				:= { 1, 0, 0, 0, 0, 0, 0, 0, }
		: VAR	: ARRAY[0..7] OF USINT	
				:= { 10, 0, 0, 0, 0, 0, 0, 0, }

5.3 I/O A/D

1)



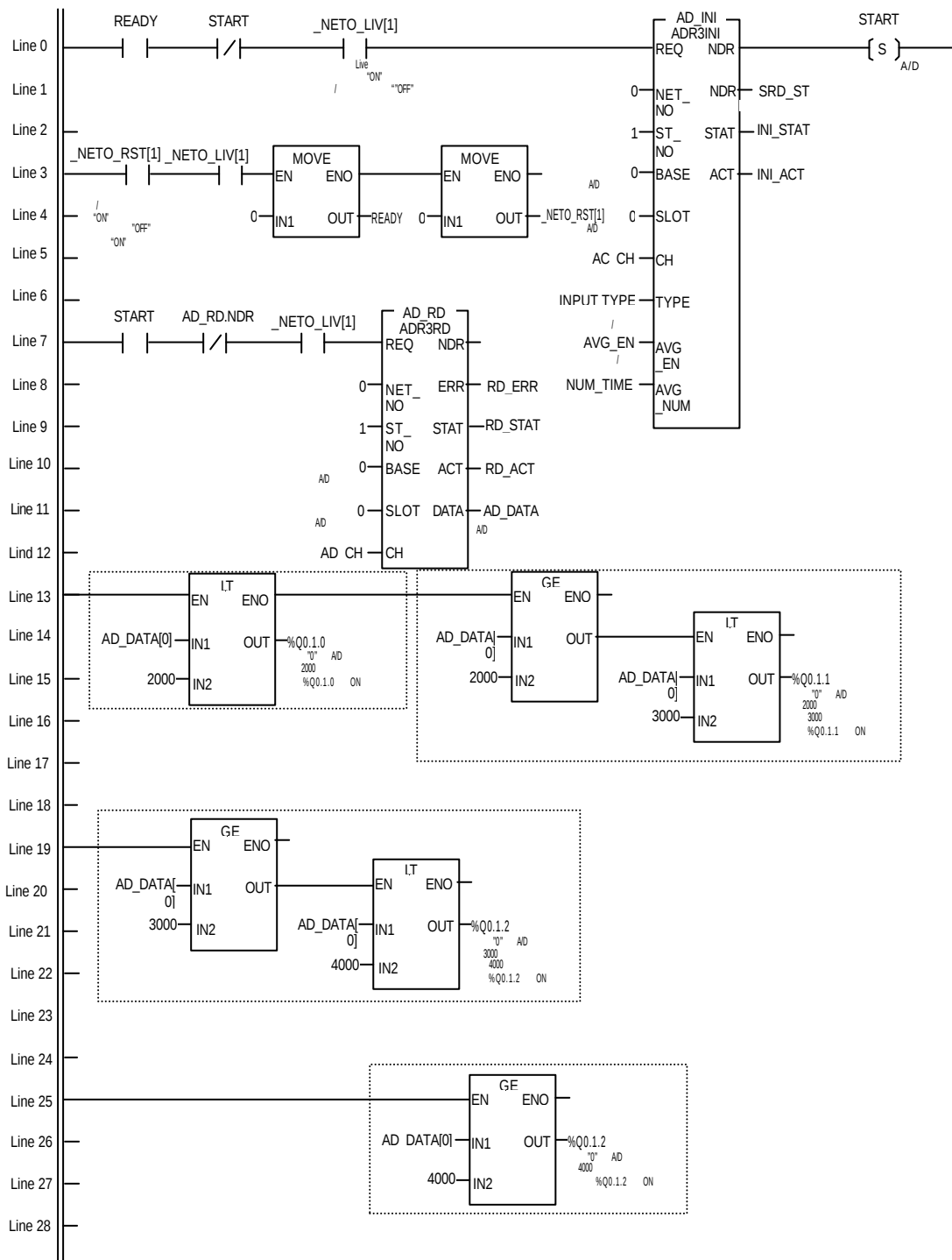
2)

- (1)AD 가 : 0
- (2) : DC 4~20mA
- (3) : 0(:50)

3)

- | | | | | | |
|-----|---|----|-------|--------|-----------|
| (1) | 0 | AD | 2,000 | %Q0.10 | On |
| (2) | 0 | AD | 2,000 | 3,000 | %Q0.11 On |
| (3) | 0 | AD | 3,000 | 4,000 | %Q0.12 On |
| (4) | 0 | AD | 4,000 | %Q0.13 | On |

4)



6

A/D PLC CPU
G3F-AD3A, G4F-AD3A

가

6.1

6.1.1. G3F-AD3A, G4F-AD3A

(10)				
0		On(1) : , Off(0) :		/ 가
1	/	On(1) : DC0~10V, Off(0) : DC1~5, DC4~20mA Off(0) .	/ 가 DC4~20mA	/ 가
2		On(1) : , Off(0) :		/ 가
3	"0"	: 2 ~ 255	"2"	/ 가
4	"1"		"2"	/ 가
5	"2"		"2"	/ 가
6	"3"		"2"	/ 가
7	"4"		"2"	/ 가
8	"5"		"2"	/ 가
9	"6"		"2"	/ 가
10	"7"		"2"	/ 가
11	SET	On(1) : 0~10 Off(0) : 0~10		/ 가
12	"0" A/D	-	-	가
13	"1" A/D		-	가
14	"2" A/D		-	가
15	"3" A/D		-	가
16	"4" A/D		-	가
17	"5" A/D		-	가
18	"6" A/D		-	가
19	"7" A/D		-	가
20		On(1) : , Off(0) :	-	가
21	"0"	0 : 17 :	-	가
22	"1"		-	가
23	"2"		-	가
24	"3"		-	가
25	"4"		-	가
26	"5"		-	가
27	"6"		-	가
28	"7"		-	가

6.1.2. G6F-AD2A

(10)				
0		On(1): , Off(0):		/ 가
1		On(1) : -2048~2047 Off(0) : -48~4047	-48~4047	/ 가
2		On(1): , Off(0) :		/ 가
3	"0"	: 2 ~ 255	"2"	/ 가
4	"1"		"2"	/ 가
5	"2"		"2"	/ 가
6	"3"		"2"	/ 가
7	SET	On(1) : 0 ~ 6 Off(0) : 0 ~ 6		/ 가 / 가
8	"0" A/D	-	-	가
9	"1" A/D		-	가
10	"2" A/D		-	가
11	"3" A/D		-	가
12		On(1): , Off(0) :	-	가
13	"0"	0 : 17 :	-	가
14	"2"		-	가
15	"3"		-	가
16	"4"		-	가

6.2

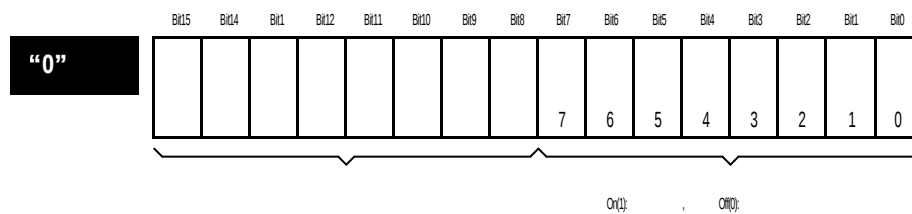
1 (Word) 16 (Bit)

16 On "1" Off "0"

G6F-AD2A Bit4~Bit15

6.2.1

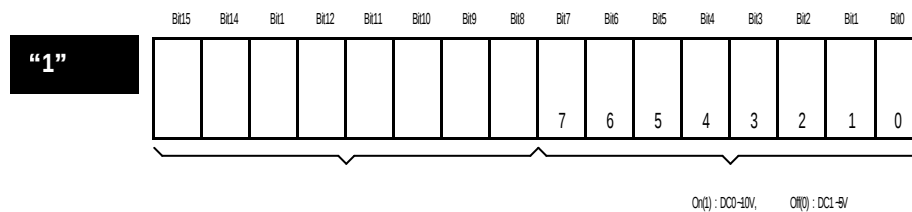
- 1) AD 가/
- 2)
- 3)
- 4) AD 가/



6.2.2

(G3F-AD3A, G4F-AD3A)

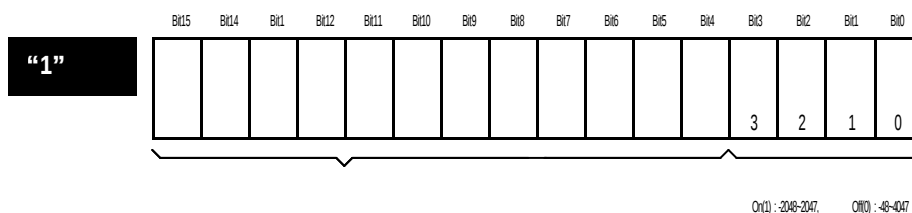
- 1)
- 2) DC1 -5V
- 3)
- 4) Off(0)



6.2.3

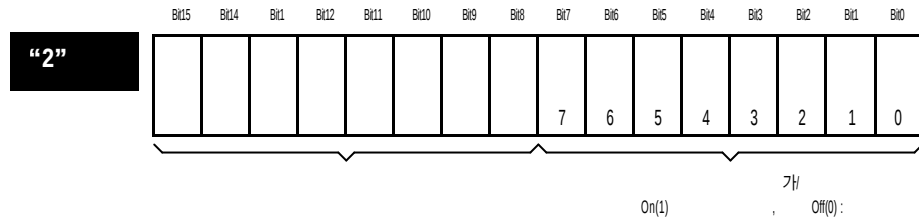
(G6F-AD2A)

- 1)



6.2.4 가/

- 1) 가/
- 2)
- 3) 가/



6.2.5

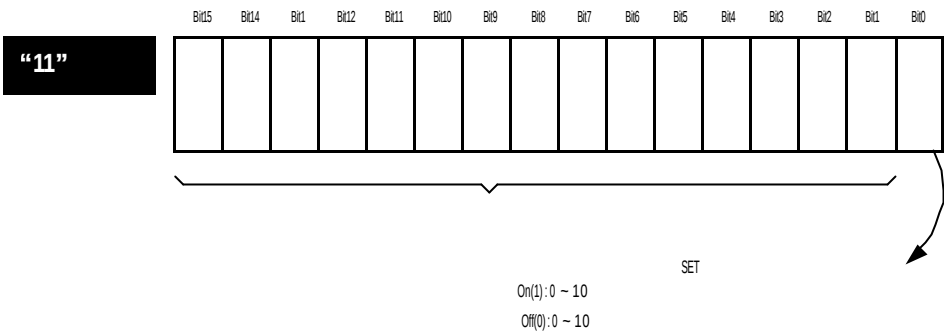
- 1) "2 ~255"
- 2) "2"
- 2)

G3F-AD3A,G4F-AD3A		G6F-AD2A	
3	0	3	0
4	1	4	1
5	2	5	2
6	3	6	3
7	4		
8	5		
9	6		
10	7		

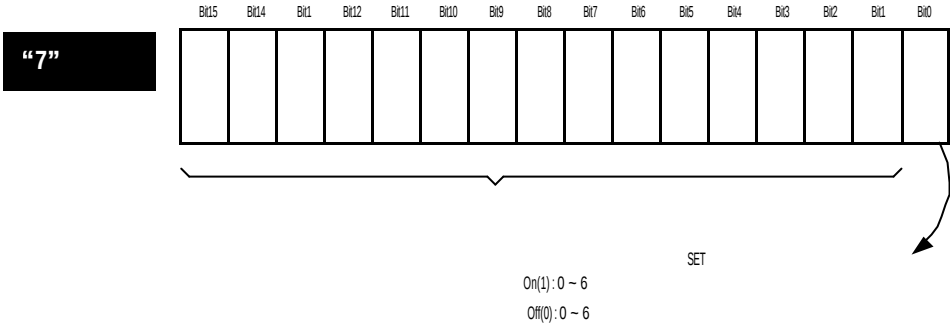
6.2.6 SET

- 1) SET On(1) 가 0~10(G6F-AD2A 0~6)
- 2) 0 On(1) 가 0 ~ 10 (G6F-AD2A 0 ~ 6)
- 3) SET

(1) G3F-AD3A,G4F-AD3A



(2) G6F-AD2A



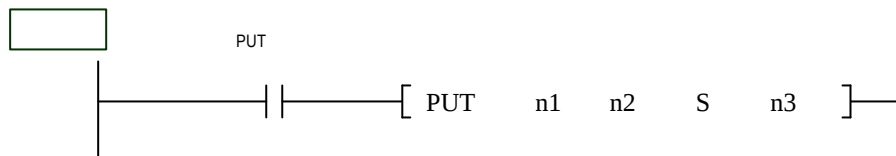
6.2.7

- 1)
- 2)

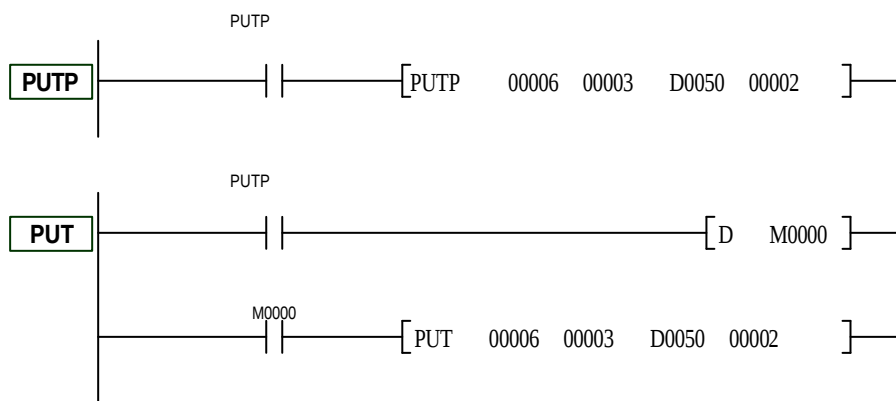
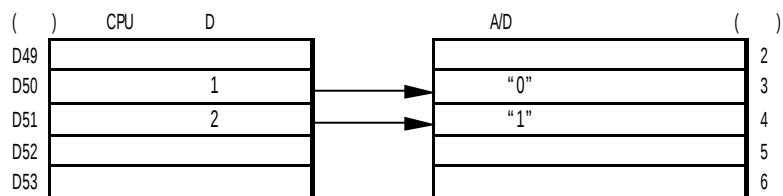
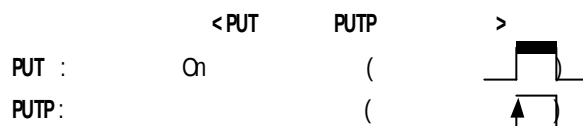
6.2.8

0	
17	

7.1.2 - PUT, PUTP



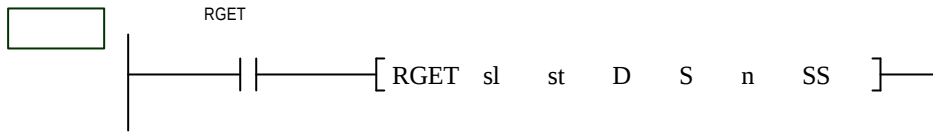
n1		
n2	Write	
S	Write 가 Device	M, P, K, L, T, C, D, #D
n3	Write	



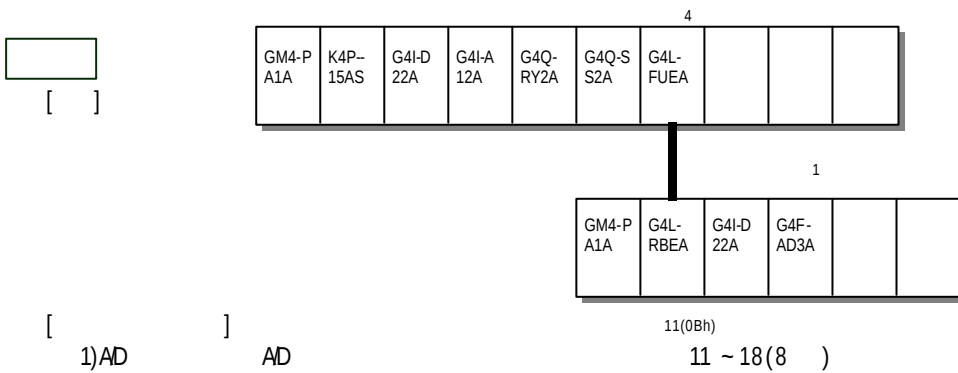
7.2

7.2.1

- RGET



sl	AB CD (AB):AD G3F-AD3A:h40,G4F-AD3A:hC0,G6F-AD2A:h02 (CD): (FUEA) (8bit) (8 bit) : 0 ~7	
st	EF GH (EF): AD : 0 ~31 (GH): (FUEA) : 0 ~63 (8bit) (8 bit)	
D	Read Device	M,P,K,L,T,C,D,#D
S	Read	
n	Read	,D
SS		M,P,K,L,T,C,D,#D



[]

1)AD

2)

3)

AD
D0300

D0001

[]

RGET

RGET hC004

h010B

D0300

00011

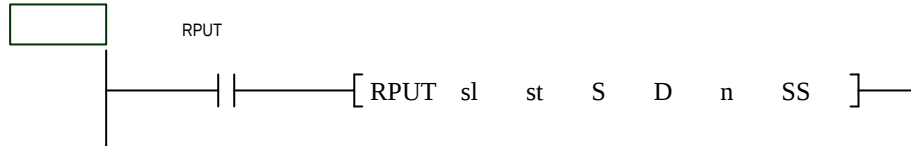
00008

D0001

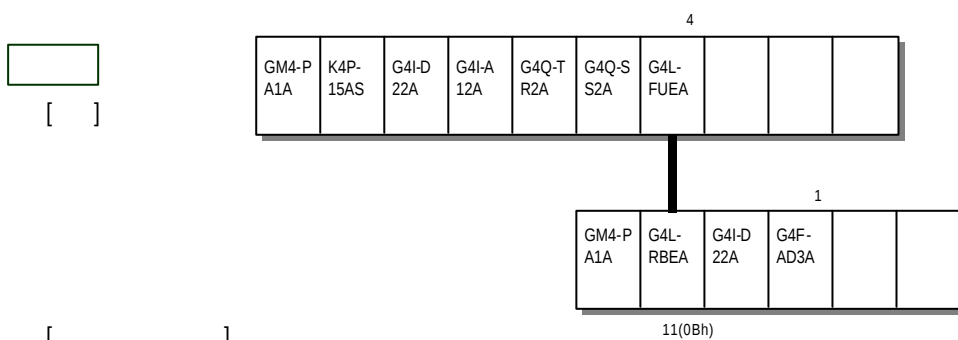
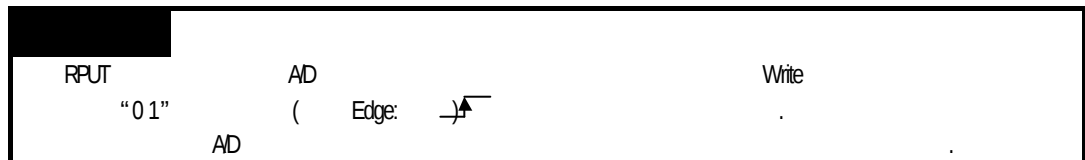
[]

7.2.2

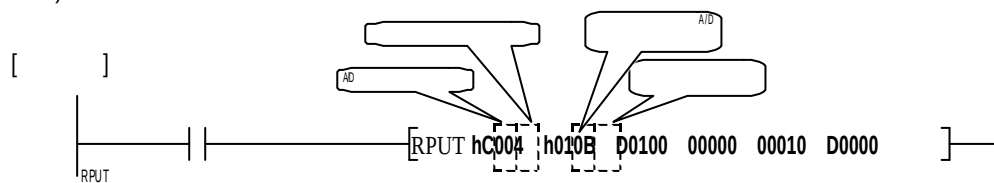
- RPUT



sl	(AB):AD AB CD G3F-AD3A:h40, G4F-AD3A:hC0, G6F-AD2A:h02 (8bit) (8 bit) (CD): (FUEA) : 0 ~7	
st	(EF): AD : 0 ~31 (GH): (FUEA) : 0 ~63 EF GH (8bit) (8 bit)	
S	Write 7 Device	M,P,K,L,T,C,D,#D
D	Write	
n	Write	,D
SS		M,P,K,L,T,C,D,#D



[1)CPU Device D100 ~ D109(10)
 2)AD 0 ~ 9 Write
 3) D0000



8 MK

8.1

-A/D

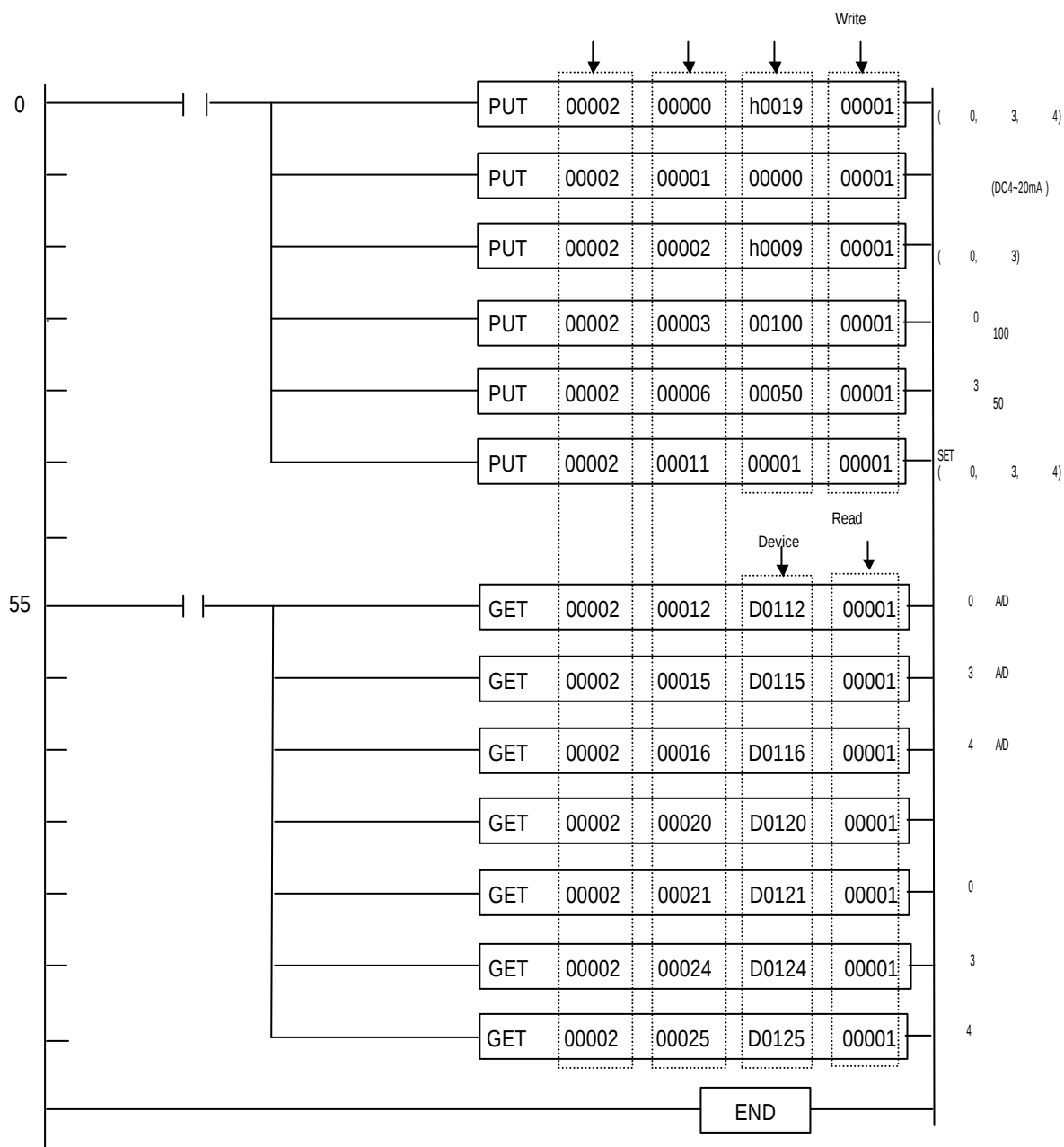
-A/D

2

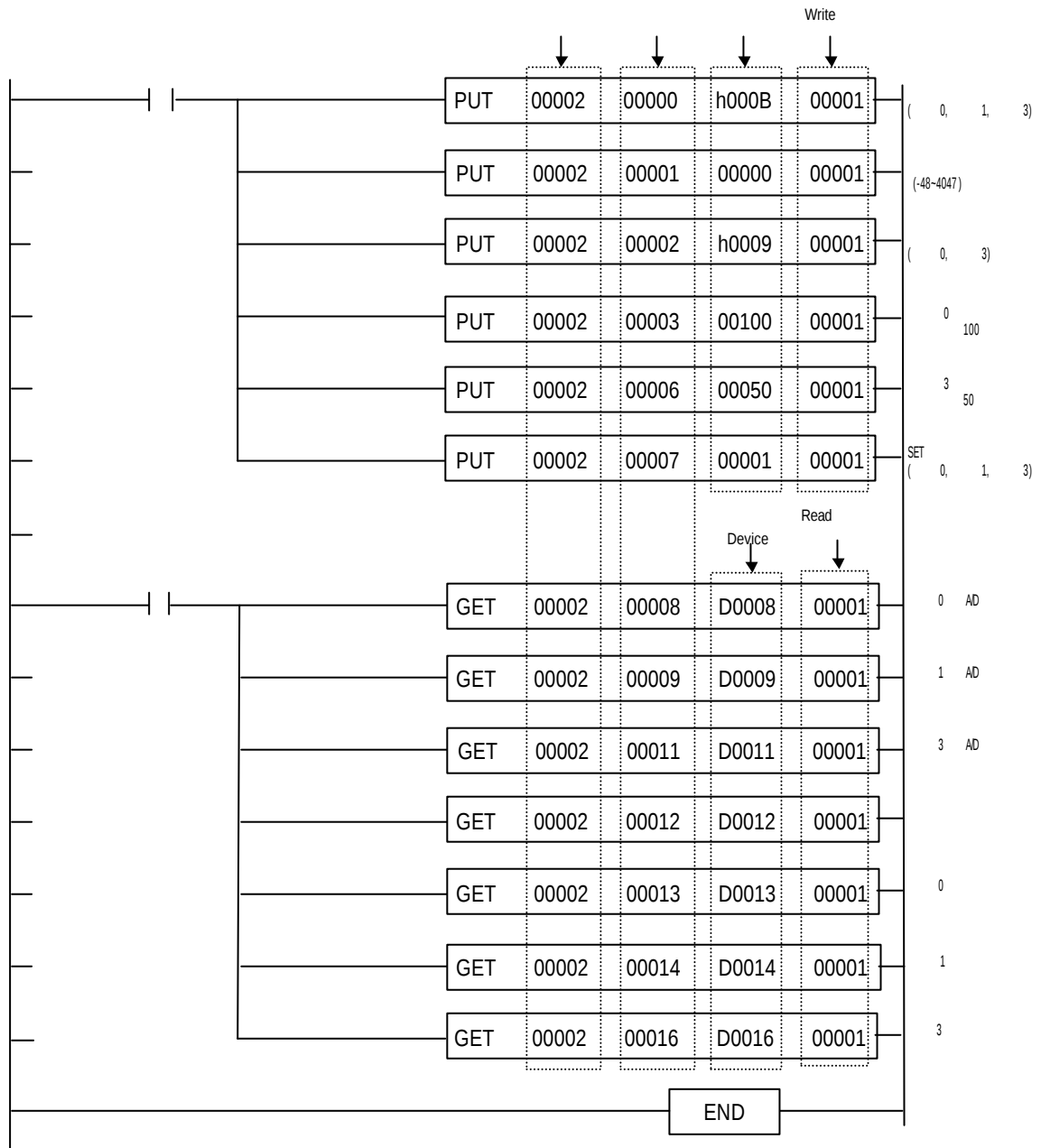
-A/D

16

8.1.1 G3F-AD3A, G4F-AD3A



8.1.2 G6F-AD2A



8.2

8.2.1 A/D

1)

GM3-PA2A	K7P-30AS	G3F-AD3A	G3Q-RY4A
----------	----------	----------	----------

2)

				Write	
1		0, 2, 4	0	H0015 00021	
2		DC4~20mA	1	H0000 0000	
3		2, 4	2	H0014 00020	2: 2 On 4: 4 On
4		2 :100	5	H0064 00100	
		4 :50	7	H0032 00050	
	SET	—	11	H0001 00001	

3)

(1)	0	2000	P0010	On	
(2)	2	3600	P0011	On	
(3)	4	2000	3600	P0012	On
(4)	4	2800	P0013	On	

4)

CMP, CMPP, DCMP, DCMPP

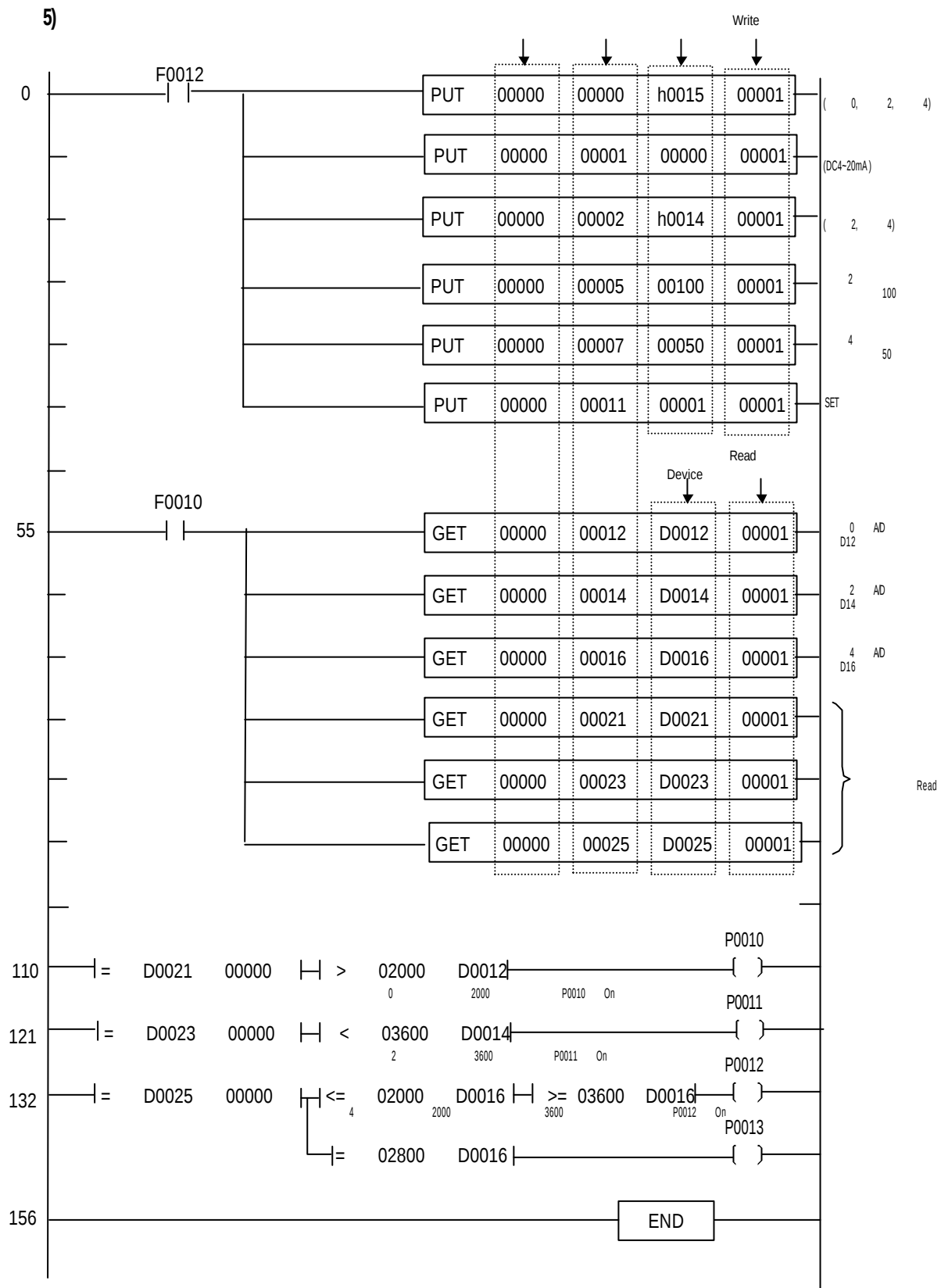
A/D

Unsigned

LOAD =, LOAD >, AND =

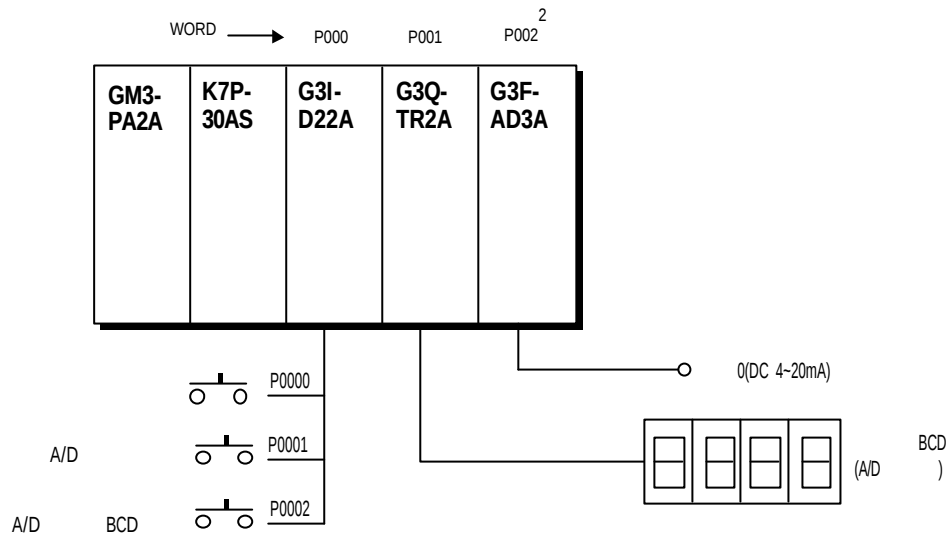
{

)



8.2.2 A/D BCD

1)



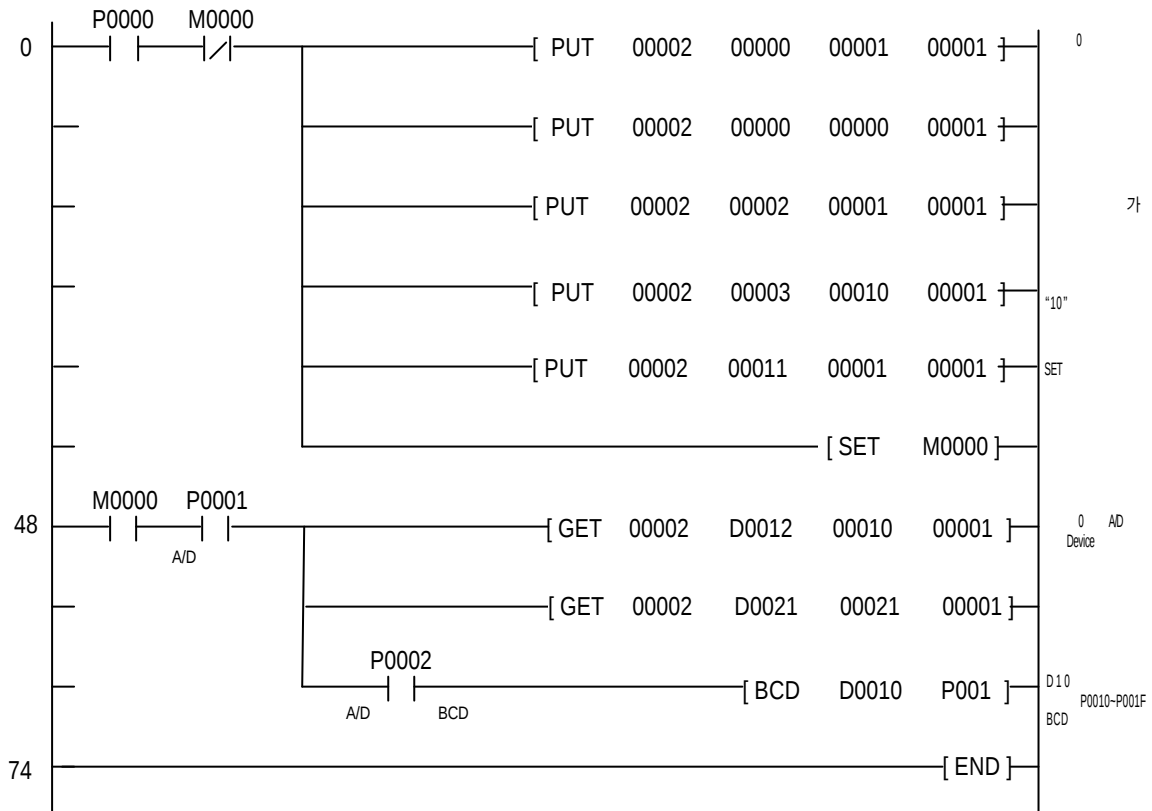
2)

- (1) : 0
 (2) : (DC 4~20mA)
 (3) :10

3)

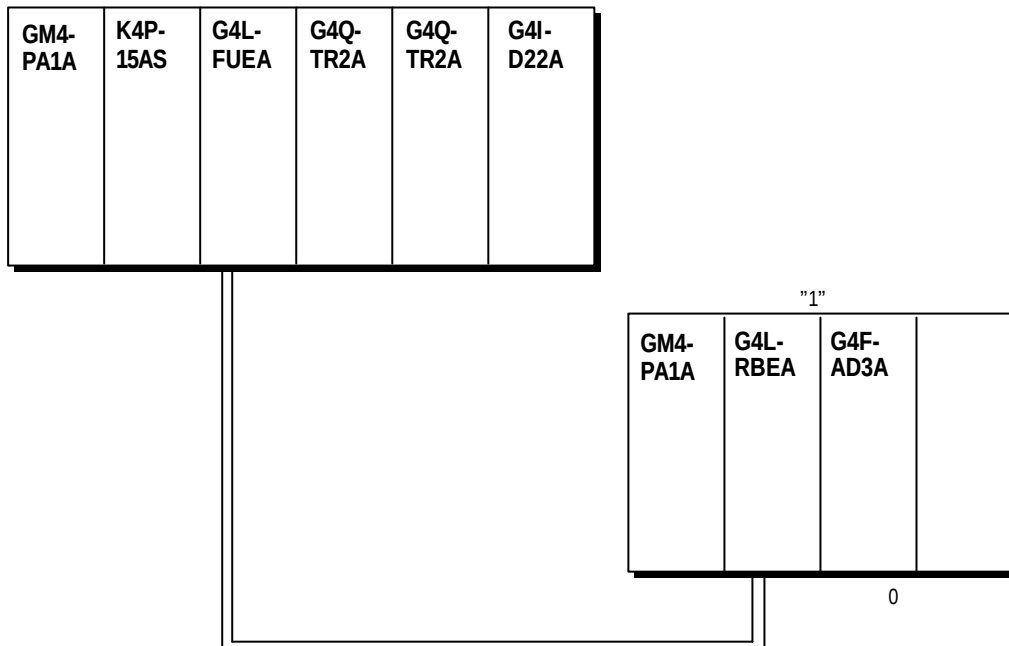
- (1) P0000 가 ON A/D .
 (2) P0001 ON A/D D10 .
 (3) P0002 가 ON A/D BCD .(P0010~P001F)

4)



8.2.3 I/O A/D

1)



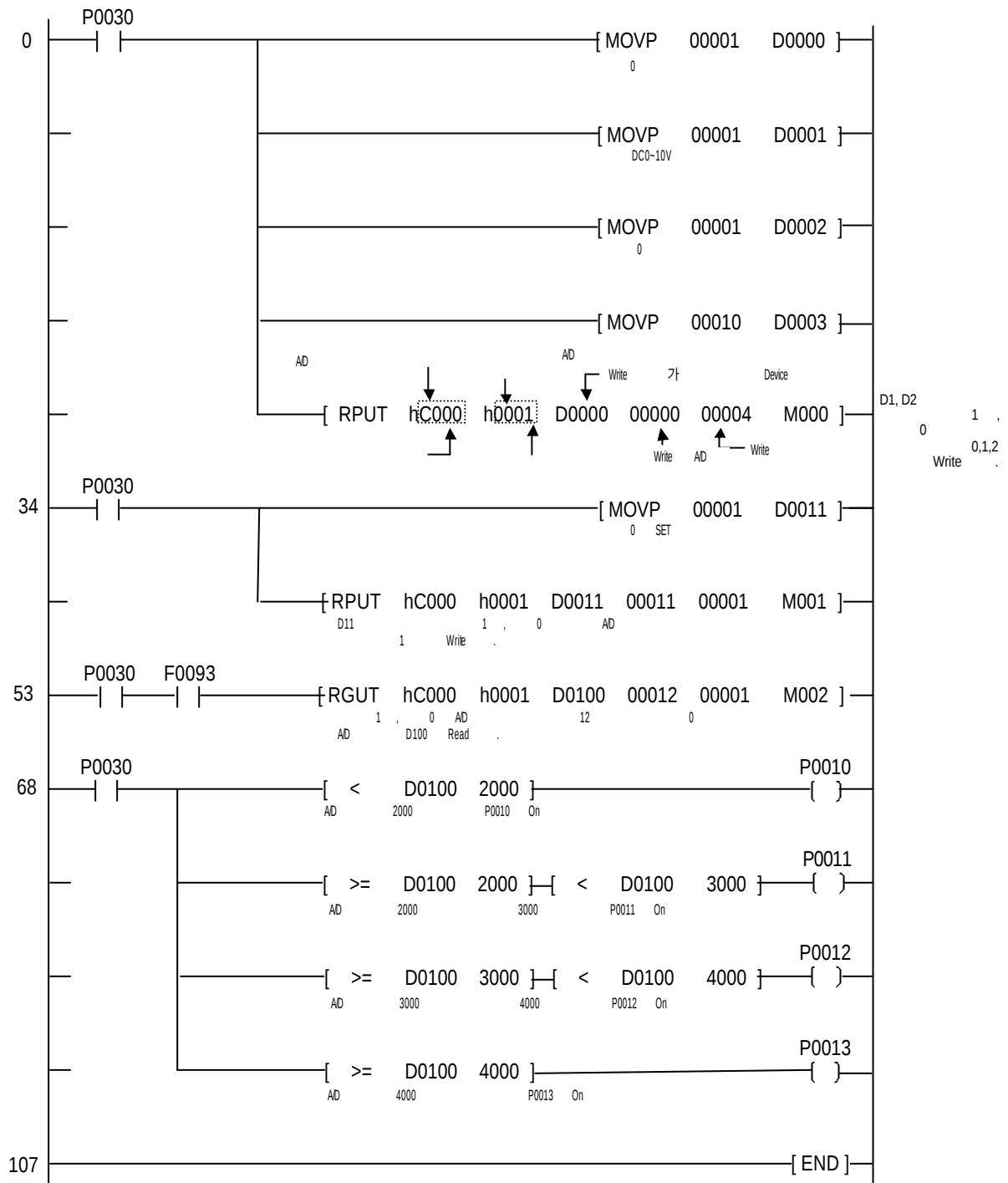
2)

- 1) : 0
- 2) : (DC 0~10V)
- 3) : 0 (:10)

3)

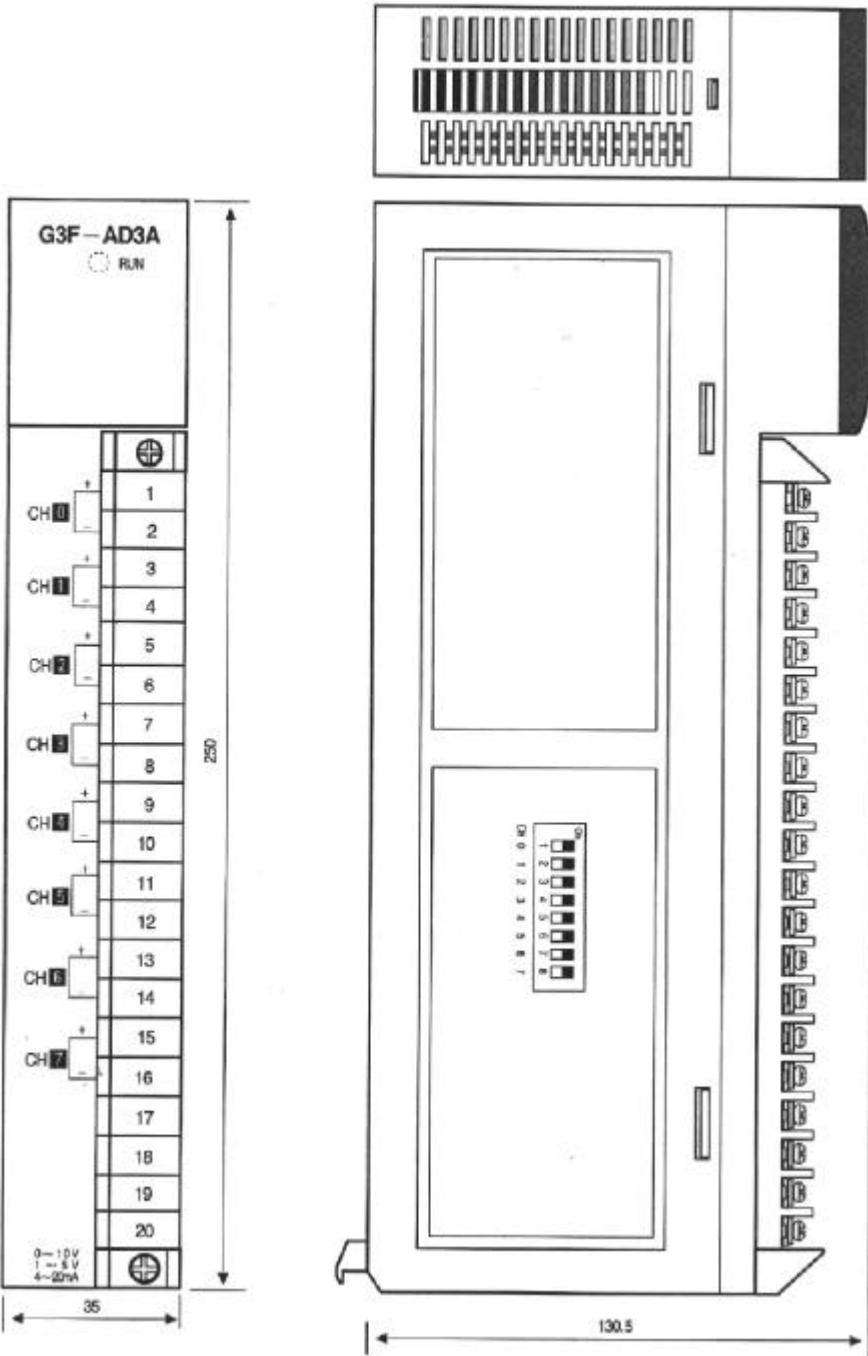
- 1) 0 A/D 2000 P0010 On
- 2) 0 A/D 2000 3000 P0011 On
- 3) 0 A/D 3000 4000 P0012 가 On
- 4) 0 A/D 4000 P0013 On

4)



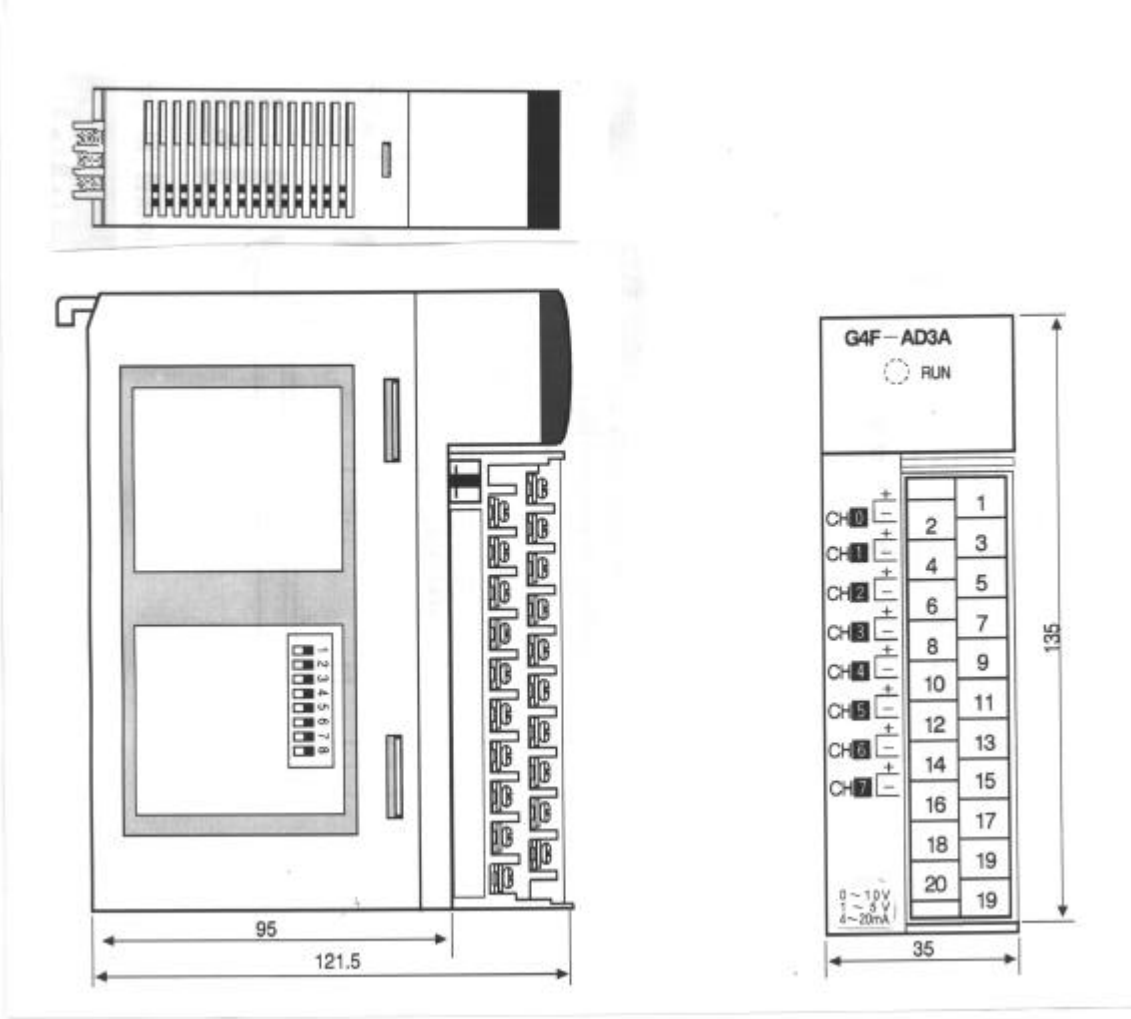
9.1 G3F-AD3A

(: mm)



9.2 G4F-AD3A

(: mm)



9.3 G6F-AD2A

(: mm)

